

Language formalisation goes mainstream

Verified Software Workshop 2019

Andreas Rossberg
Dfinity

The Definition of Standard ML

Robin Milner

Mads Tofte

Robert Harper

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WebAssembly Language formalisation goes mainstream

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WebAssembly

Wasm

~~WASM~~

Wasm

a virtual instruction set architecture
that is fully sandboxed
and freely embeddable anywhere

a low-level virtual machine



fastly[®]

D F I N I T Y

Ethereum

parity

open standard (W3C, github)

Goals & Constraints

Semantics

Language-independent

Platform-independent

Hardware-independent

Fast to execute

Safe to execute

Deterministic

Easy to reason about

Representation

Compact

Easy to generate

Fast to decode

Fast to validate

Fast to compile

Streamable

Parallelisable

modular & sandboxed

binaries are modules

function from explicit imports to exports

encapsulated, no access to internals

sandboxed, no ambient capabilities

type system

simple, linear-time type system

type-safe and memory-safe

no undefined behaviour

custom loader could implement extensions

formal semantics

from the get-go!

textbook techniques

meta-mission: raise the bar
for real-world language specs

static semantics

typing, a.k.a. validation

inference rules

$$\Gamma \vdash e^* : t_1^* \rightarrow t_2^*$$

stack machine

(i32.const 42)	$: \varepsilon \rightarrow i32$	}
(local.get \$x)	$: \varepsilon \rightarrow \Gamma(\$x)$	
(i32.add)	$: i32 \ i32 \rightarrow i32$	

$$\Gamma \vdash t.\mathbf{const}\ c : \varepsilon \rightarrow t$$

$$\Gamma \vdash t.\mathbf{add} : t\ t \rightarrow t$$

$$\frac{\Gamma(\textcolor{red}{\$x}) = t}{\Gamma \vdash \mathbf{local.get}\ \$x : \varepsilon \rightarrow t}$$

$$\frac{\Gamma \vdash e_0 : t_1^* \rightarrow t_3^* \quad \Gamma \vdash e^* : t_3^* \rightarrow t_2^*}{\Gamma \vdash e_0 \ e^* : t_1^* \rightarrow t_2^*}$$

$$\frac{\Gamma \vdash e^* : t_1^* \rightarrow t_2^*}{\Gamma \vdash e^* : t_0^* \ t_1^* \rightarrow t_0^* \ t_2^*}$$

structured control flow

$\$l : t_2^*$

(block \$l ($t_1^* \rightarrow t_2^*$)

...

(br \$l) : $t_2^* \rightarrow \perp$

...

end)

$\$l : t_1^*$

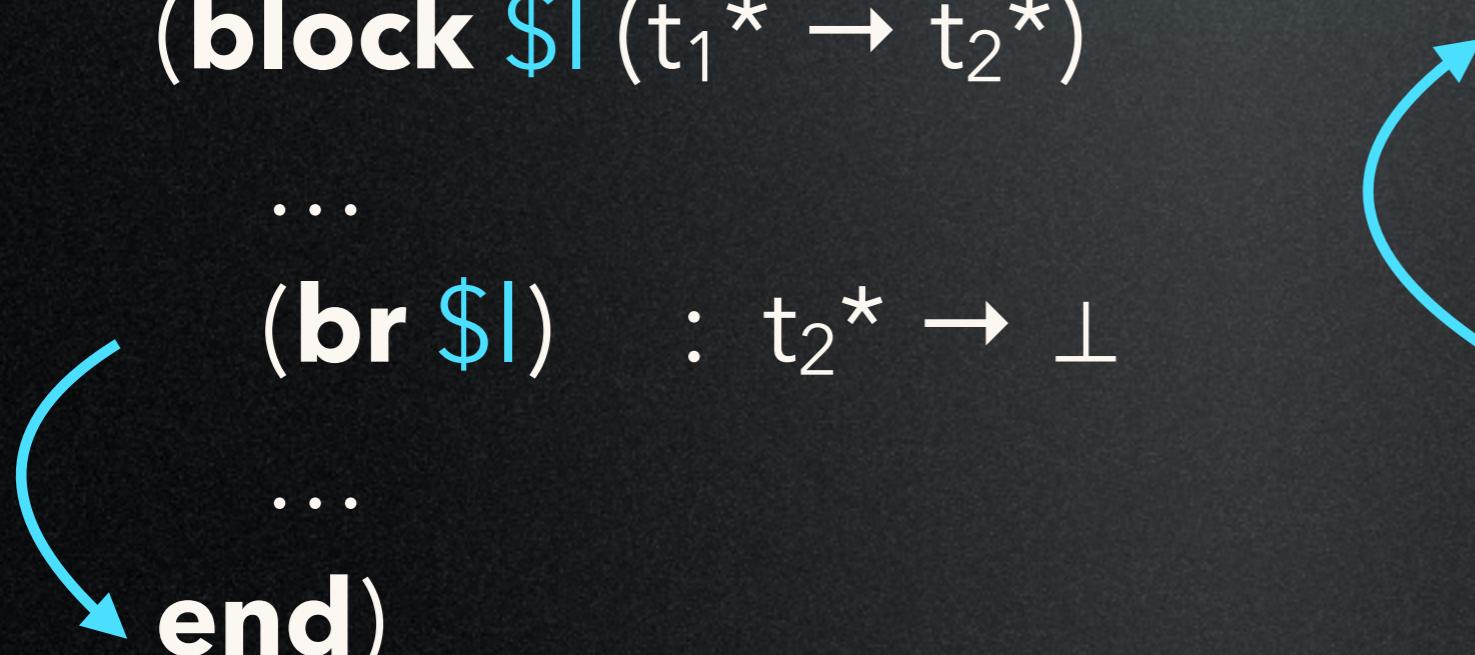
(loop \$l ($t_1^* \rightarrow t_2^*$)

...

(br \$l) : $t_1^* \rightarrow \perp$

...

end)



$$\frac{\Gamma, \$| : t_2^* \vdash e^* : t_1^* \rightarrow t_2^*}{\Gamma \vdash \mathbf{block} \; \$| \; e^* \; \mathbf{end} : t_1^* \rightarrow t_2^*}$$

$$\frac{\Gamma(\$|) = t^*}{\Gamma \vdash \mathbf{br} \; \$| : t^* \rightarrow \perp}$$

$$\frac{\Gamma, \$| : t_1^* \vdash e^* : t_1^* \rightarrow t_2^*}{\Gamma \vdash \mathbf{loop} \; \$| \; e^* \; \mathbf{end} : t_1^* \rightarrow t_2^*}$$

$$\frac{\Gamma, \$| : t_2^* \vdash e^* : t_1^* \rightarrow t_2^*}{\Gamma \vdash \mathbf{block} \; \$| \; e^* \; \mathbf{end} : t_1^* \rightarrow t_2^*}$$

$$\frac{\Gamma(\$|) = t^*}{\Gamma \vdash \mathbf{br} \; \$| : t^* \rightarrow \perp}$$

$$\frac{\Gamma, \$| : t_1^* \vdash e^* : t_1^* \rightarrow t_2^*}{\Gamma \vdash \mathbf{loop} \; \$| \; e^* \; \mathbf{end} : t_1^* \rightarrow t_2^*}$$

$$\frac{\Gamma, \$| : t_2^* \vdash e^* : t_1^* \rightarrow t_2^*}{\Gamma \vdash \mathbf{block} \; \$| \; e^* \; \mathbf{end} : t_1^* \rightarrow t_2^*}$$

$$\frac{\Gamma(\$|) = t^*}{\Gamma \vdash \mathbf{br} \; \$| : t_1^* \; t^* \rightarrow t_2^*}$$

$$\frac{\Gamma, \$| : t_1^* \vdash e^* : t_1^* \rightarrow t_2^*}{\Gamma \vdash \mathbf{loop} \; \$| \; e^* \; \mathbf{end} : t_1^* \rightarrow t_2^*}$$

dynamic semantics

structured operational semantics

small-step reduction rules

$$s; e^* \xrightarrow{*} s'; e'^*$$

$(t.\mathbf{const} c_1)(t.\mathbf{const} c_2) t.\mathbf{add} \rightarrow (t.\mathbf{const} c_1 + c_2)$

(values) $v ::= (t.\mathbf{const} c)$

(eval contexts) $E ::= v^* [] e^* \mid (\mathbf{label}\{e^*\} E \mathbf{end})$

(block e* **end**) \mapsto (**label**{ ε } e* **end**)

(loop e* **end**) \mapsto (**label**{**loop** e* **end**} e* **end**)

(**label**{e*} v* **end**) \mapsto v*

(**label**{e*} Lⁿ[v* (**br** n)] **end**) \mapsto v* e*

(label contexts) L⁰ ::= v* [] e*

Lⁿ⁺¹ ::= v* (**label**{e*} Lⁿ **end**) e*

$$\frac{s.\text{mem}[i..i+|t|-1] = \text{bytes}_t(c)}{s; (\mathbf{i32.const} i) t.\mathbf{load} \rightarrow s; (t.\mathbf{const} c)}$$

$$\frac{s' = s \text{ with } \text{mem}[i..i+|t|-1] = \text{bytes}_t(c)}{s; (\mathbf{i32.const} i) (t.\mathbf{const} c) t.\mathbf{store} \rightarrow s'; \varepsilon}$$

soundness

If $\vdash e^* : \varepsilon \rightarrow t^*$ and $\vdash s$,
then $s; e^* \vdash^* s'; v^*$
and $\vdash v^* : \varepsilon \rightarrow t^*$ and $\vdash s'$.

(value types)	$t ::=$	i32 i64 f32 f64
(packed types)	$pt ::=$	i8 i16 i32
(function types)	$ft ::=$	$t^* \rightarrow t^*$
	$unop ::=$	neg abs ...
	$binop ::=$	add sub mul div_s div_u ...
	$relop ::=$	eq ne lt gt ...
	$cvttop ::=$	convert/t reinterpret/t
(instructions)	$e ::=$	$t.\mathbf{const} c$ $t.unop$ $t.binop$ $t.relop$ $t.cvttop$ unreachable nop drop select block $ft\ e^*$ end loop $ft\ e^*$ end if $ft\ e^*$ else e^* end br i br_if i br_table $i^*\ i$ call i call_indirect ft return get_local i set_local i tee_local i get_global i set_global i $t.\mathbf{load}\ pt? n$ $t.\mathbf{store}\ pt? n$ current_mem grow_mem
(functions)	$func ::=$	func $ft\ (\mathbf{local}\ t)^*\ e^*$
(globals)	$glob ::=$	global $\mathbf{mut}?\ t\ e^*$
(tables)	$tab ::=$	table $n\ i^*$
(memories)	$mem ::=$	memory n
(modules)	$m ::=$	module $import^*\ func^*\ glob^*\ tab? mem? export^*$

(store)	s	::= {inst $inst^*$, tab $tabinst^*$, mem $meminst^*$ }
(instances)	$inst$::= {func cl^* , glob v^* , tab $i^?$, mem $i^?$ }
	$tabinst$::= cl^*
	$meminst$::= b^*
(closures)	cl	::= {inst i , code f } (where f is not an import and has all exports ex^* erased)
(values)	v	::= $t.\text{const } c$
(administrative operators)	e	::= ... trap call cl label{ t^* ; e^* } e^* end local{ i ; v^* } e^* end
(local contexts)	L^0	::= $v^* [.] e^*$
	L^{k+1}	::= $v^* \text{label}\{t^*; e^*\} L^k \text{end } e^*$

$$\text{Reduction} \quad \frac{s; v^*; e^* \xrightarrow{i} s'; v'^*; e'^*}{s; v^*; L^k[e^*] \xrightarrow{i} s'; v'^*; L^k[e'^*]} \quad \frac{s; v^*; e^* \xrightarrow{i} s'; v'^*; e'^*}{s; v_0^*; \text{local}\{i; v^*\} e^* \text{ end} \xrightarrow{j} s'; v_0^*; \text{local}\{i; v'^*\} e'^* \text{ end}} \quad \boxed{s; v^*; e^* \xrightarrow{i} s; v^*; e^*}$$

$(t.\text{const } c) t.\text{unop}$	$\xrightarrow{} t.\text{const } unop_t(c)$	
$(t.\text{const } c_1) (t.\text{const } c_2) t.\text{binop}$	$\xrightarrow{} t.\text{const } c$	if $c = binop_t(c_1, c_2)$
$(t.\text{const } c_1) (t.\text{const } c_2) t.\text{binop}$	$\xrightarrow{} \text{trap}$	otherwise
$(t.\text{const } c) t.\text{testop}$	$\xrightarrow{} i32.\text{const } testop_t(c)$	
$(t.\text{const } c_1) (t.\text{const } c_2) t.\text{relop}$	$\xrightarrow{} i32.\text{const } relop_t(c_1, c_2)$	
$(t_1.\text{const } c) t_2.\text{convert } t_1_{sx}^?$	$\xrightarrow{} t_2.\text{const } c'$	if $c' = cvt_{t_1, t_2}^{sx} (c)$
$(t_1.\text{const } c) t_2.\text{convert } t_1_{sx}^?$	$\xrightarrow{} \text{trap}$	otherwise
$(t_1.\text{const } c) t_2.\text{reinterpret } t_1$	$\xrightarrow{} t_2.\text{const } const_{t_2}(\text{bits}_{t_1}(c))$	
unreachable	$\xrightarrow{} \text{trap}$	
nop	$\xrightarrow{} \epsilon$	
$v \text{ drop}$	$\xrightarrow{} \epsilon$	
$v_1 v_2 (\text{i32.const } 0) \text{ select}$	$\xrightarrow{} v_2$	
$v_1 v_2 (\text{i32.const } k + 1) \text{ select}$	$\xrightarrow{} v_1$	
$v^n \text{ block } (t_1^n \rightarrow t_2^n) e^* \text{ end}$	$\xrightarrow{} \text{label}\{t_2^n; \epsilon\} v^n e^* \text{ end}$	
$v^n \text{ loop } (t_1^n \rightarrow t_2^n) e^* \text{ end}$	$\xrightarrow{} \text{label}\{t_1^n; \text{loop } (t_1^n \rightarrow t_2^n) e^* \text{ end}\} v^n e^* \text{ end}$	
$(\text{i32.const } 0) \text{ if } tf e_1^* \text{ else } e_2^* \text{ end}$	$\xrightarrow{} \text{block } tf e_2^* \text{ end}$	
$(\text{i32.const } k + 1) \text{ if } tf e_1^* \text{ else } e_2^* \text{ end}$	$\xrightarrow{} \text{block } tf e_1^* \text{ end}$	
$\text{label}\{t^*; e^*\} v^* \text{ end}$	$\xrightarrow{} v^*$	
$\text{label}\{t^*; e^*\} \text{ trap end}$	$\xrightarrow{} \text{trap}$	
$\text{label}\{t^n; e^*\} L^j[v^n(\text{br } j)] \text{ end}$	$\xrightarrow{} v^n e^*$	
$(\text{i32.const } 0) (\text{br_if } j)$	$\xrightarrow{} \epsilon$	
$(\text{i32.const } k + 1) (\text{br_if } j)$	$\xrightarrow{} \text{br } j$	
$(\text{i32.const } k) (\text{br_table } j_1^k j j_2^*)$	$\xrightarrow{} \text{br } j$	
$(\text{i32.const } k + n) (\text{br_table } j_1^k j)$	$\xrightarrow{} \text{br } j$	
$s; \text{call } j$	$\xrightarrow{i} \text{call } s_{\text{func}}(i, j)$	
$s; (\text{i32.const } j) \text{ call_indirect } tf$	$\xrightarrow{i} \text{call } s_{\text{tab}}(i, j)$	if $s_{\text{tab}}(i, j)_{\text{code}} = (\text{func } tf \text{ local } t^* e^*)$
$s; (\text{i32.const } j) \text{ call_indirect } tf$	$\xrightarrow{i} \text{trap}$	otherwise
$v^n (\text{call } cl)$	$\xrightarrow{} \text{local}\{cl_{\text{inst}}; v^n (\text{i32.const } 0)^k\} \text{ block } (\epsilon \rightarrow t_2^m) e^* \text{ end end} \dots$	
$\text{local}\{i; v_l^*\} v^* \text{ end}$	$\xrightarrow{} v^*$... if $cl_{\text{code}} = (\text{func } (t_1^n \rightarrow t_2^m) \text{ local } t^k e^*)$
$\text{local}\{i; v_l^*\} \text{ trap end}$	$\xrightarrow{} \text{trap}$	
$\text{local}\{i; v_l^*\} L^{k+1}[\text{return}] \text{ end}$	$\xrightarrow{} \text{local}\{i; v_l^*\} L^{k+1}[\text{br } k] \text{ end}$	
$v_1^j v_2^k; \text{get_local } j$	$\xrightarrow{} v$	
$v_1^j v_2^k; v' (\text{set_local } j)$	$\xrightarrow{} v_1^j v' v_2^k; \epsilon$	
$v (\text{tee_local } j)$	$\xrightarrow{} v v (\text{set_local } j)$	
$s; \text{get_global } j$	$\xrightarrow{i} s_{\text{glob}}(i, j)$	
$s; v (\text{set_global } j)$	$\xrightarrow{i} s'; \epsilon$	if $s' = s$ with $\text{glob}(i, j) = v$
$s; (\text{i32.const } k) (\text{t.load } a o)$	$\xrightarrow{i} t.\text{const } const_t(b^*)$	if $s_{\text{mem}}(i, k + o, t) = b^*$
$s; (\text{i32.const } k) (\text{t.load } tp_{sx} a o)$	$\xrightarrow{i} t.\text{const } const_t^{sx}(b^*)$	if $s_{\text{mem}}(i, k + o, tp) = b^*$
$s; (\text{i32.const } k) (\text{t.load } tp_{sx}^? a o)$	$\xrightarrow{i} \text{trap}$	otherwise
$s; (\text{i32.const } k) (\text{t.const } c) (\text{t.store } a o)$	$\xrightarrow{i} s'; \epsilon$	if $s' = s$ with $\text{mem}(i, k + o, t) = \text{bits}_t^{ t }(c)$
$s; (\text{i32.const } k) (\text{t.const } c) (\text{t.store } tp a o)$	$\xrightarrow{i} s'; \epsilon$	if $s' = s$ with $\text{mem}(i, k + o, tp) = \text{bits}_t^{ tp }(c)$
$s; (\text{i32.const } k) (\text{t.const } c) (\text{t.store } tp^? a o)$	$\xrightarrow{i} \text{trap}$	otherwise
$s; \text{current_memory}$	$\xrightarrow{i} i32.\text{const } s_{\text{mem}}(i, *) /64 \text{ Ki}$	
$s; (\text{i32.const } k) \text{ grow_memory}$	$\xrightarrow{i} s'; i32.\text{const } s_{\text{mem}}(i, *) /64 \text{ Ki}$	if $s' = s$ with $\text{mem}(i, *) = s_{\text{mem}}(i, *) (0)^{k \cdot 64 \text{ Ki}}$
$s; (\text{i32.const } k) \text{ grow_memory}$	$\xrightarrow{i} i32.\text{const } (-1)$	

Figure 1. Small-step reduction rules

(contexts) $C ::= \{\text{func } tf^*, \text{ global } tg^*, \text{ table } n^?, \text{ memory } n^?, \text{ local } t^*, \text{ label } (t^*)^*\}$

Typing Instructions

$$C \vdash e^* : tf$$

$$\begin{array}{c}
\frac{C \vdash t.\mathbf{const} c : \epsilon \rightarrow t \quad C \vdash t.\mathbf{unop} : t \rightarrow t \quad C \vdash t.\mathbf{binop} : t t \rightarrow t \quad C \vdash t.\mathbf{testop} : t \rightarrow i32 \quad C \vdash t.\mathbf{relop} : t t \rightarrow i32}{t_1 \neq t_2 \quad sx^? = \epsilon \Leftrightarrow (t_1 = \mathbf{in} \wedge t_2 = \mathbf{in}' \wedge |t_1| < |t_2|) \vee (t_1 = \mathbf{fn} \wedge t_2 = \mathbf{fn}') \quad t_1 \neq t_2 \quad |t_1| = |t_2|} C \vdash t_1.\mathbf{convert} t_2_sx^? : t_2 \rightarrow t_1 \quad C \vdash t_1.\mathbf{reinterpret} t_2 : t_2 \rightarrow t_1 \\[10pt]
\frac{C \vdash \mathbf{unreachable} : t_1^* \rightarrow t_2^* \quad C \vdash \mathbf{nop} : \epsilon \rightarrow \epsilon \quad C \vdash \mathbf{drop} : t \rightarrow \epsilon \quad C \vdash \mathbf{select} : t t i32 \rightarrow t}{tf = t_1^n \rightarrow t_2^m \quad C, \text{label}(t_2^m) \vdash e^* : tf \quad tf = t_1^n \rightarrow t_2^m \quad C, \text{label}(t_1^n) \vdash e^* : tf} C \vdash \mathbf{block} tf e^* \mathbf{end} : tf \quad C \vdash \mathbf{loop} tf e^* \mathbf{end} : tf \\[10pt]
\frac{tf = t_1^n \rightarrow t_2^m \quad C, \text{label}(t_2^m) \vdash e_1^* : tf \quad C, \text{label}(t_2^m) \vdash e_2^* : tf}{C \vdash \mathbf{if} tf e_1^* \mathbf{else} e_2^* \mathbf{end} : t_1^n i32 \rightarrow t_2^m} \\[10pt]
\frac{C_{\text{label}}(i) = t^* \quad C_{\text{label}}(i) = t^* \quad (C_{\text{label}}(i) = t^*)^+}{C \vdash \mathbf{br} i : t_1^* t^* \rightarrow t_2^* \quad C \vdash \mathbf{br_if} i : t^* i32 \rightarrow t^* \quad C \vdash \mathbf{br_table} i^+ : t_1^* t^* i32 \rightarrow t_2^*} \\[10pt]
\frac{C_{\text{label}}(|C_{\text{label}}| - 1) = t^* \quad C_{\text{func}}(i) = tf \quad tf = t_1^* \rightarrow t_2^* \quad C_{\text{table}} = n}{C \vdash \mathbf{return} : t_1^* t^* \rightarrow t_2^* \quad C \vdash \mathbf{call} i : tf \quad C \vdash \mathbf{call_indirect} tf : t_1^* i32 \rightarrow t_2^*} \\[10pt]
\frac{C_{\text{local}}(i) = t \quad C_{\text{local}}(i) = t \quad C_{\text{local}}(i) = t \quad C_{\text{global}}(i) = \mathbf{mut}^? t \quad C_{\text{global}}(i) = \mathbf{mut} t}{C \vdash \mathbf{get_local} i : \epsilon \rightarrow t \quad C \vdash \mathbf{set_local} i : t \rightarrow \epsilon \quad C \vdash \mathbf{tee_local} i : t \rightarrow t \quad C \vdash \mathbf{get_global} i : \epsilon \rightarrow t \quad C \vdash \mathbf{set_global} i : t \rightarrow \epsilon} \\[10pt]
\frac{C_{\text{memory}} = n \quad 2^a \leq (|tp| <)^? |t| \quad (tp_sz)^? = \epsilon \vee t = \mathbf{im} \quad C_{\text{memory}} = n \quad 2^a \leq (|tp| <)^? |t| \quad tp^? = \epsilon \vee t = \mathbf{im}}{C \vdash t.\mathbf{load} (tp_sz)^? a o : i32 \rightarrow t \quad C \vdash t.\mathbf{store} tp^? a o : i32 t \rightarrow \epsilon} \\[10pt]
\frac{C_{\text{memory}} = n \quad C_{\text{memory}} = n}{C \vdash \mathbf{current_memory} : \epsilon \rightarrow i32 \quad C \vdash \mathbf{grow_memory} : i32 \rightarrow i32} \\[10pt]
\frac{}{C \vdash \epsilon : \epsilon \rightarrow \epsilon} \quad \frac{C \vdash e_1^* : t_1^* \rightarrow t_2^* \quad C \vdash e_2 : t_2^* \rightarrow t_3^*}{C \vdash e_1^* e_2 : t_1^* \rightarrow t_3^*} \quad \frac{C \vdash e^* : t_1^* \rightarrow t_2^*}{C \vdash e^* : t^* t_1^* \rightarrow t^* t_2^*}
\end{array}$$

Typing Modules

$$\begin{array}{c}
\frac{tf = t_1^* \rightarrow t_2^* \quad C, \text{local } t_1^* t^*, \text{label } (t_2^*) \vdash e^* : \epsilon \rightarrow t_2^* \quad tg = \mathbf{mut}^? t \quad C \vdash e^* : \epsilon \rightarrow t \quad ex^* = \epsilon \vee tg = t}{C \vdash ex^* \mathbf{func} tf \mathbf{local} t^* e^* : ex^* tf \quad C \vdash ex^* \mathbf{global} tg e^* : ex^* tg} \\[10pt]
\frac{(C_{\text{func}}(i) = tf)^n}{C \vdash ex^* \mathbf{table} n i^n : ex^* n} \quad \frac{}{C \vdash ex^* \mathbf{memory} n : ex^* n} \\[10pt]
\frac{}{C \vdash ex^* \mathbf{func} tf im : ex^* tf} \quad \frac{tg = t}{C \vdash ex^* \mathbf{global} tg im : ex^* tg} \quad \frac{}{C \vdash ex^* \mathbf{table} n im : ex^* n} \quad \frac{}{C \vdash ex^* \mathbf{memory} n im : ex^* n} \\[10pt]
\frac{(C_i \vdash f : ex_f^* tf)^* \quad (C_i \vdash glob_i : ex_g^* tg_i)^* \quad (C \vdash tab : ex_t^* n)^? \quad (C \vdash mem : ex_m^* n)^?}{(C_i = \{\text{global } tg^{i-1}\})_i^* \quad C = \{\text{func } tf^*, \text{global } tg^*, \text{table } n^?, \text{memory } n^?\}} \quad \frac{ex_f^{**} ex_g^{**} ex_t^{*?} ex_m^{*?} \text{ distinct}}{\vdash \mathbf{module} f^* glob^* tab^? mem?}
\end{array}$$

Figure 1. Typing rules

mechanisation

OCaml [myself]

Isabelle [Conrad Watt, Cambridge]

Coq (ongoing) [Dave Swasey, MPI]

K (ongoing) [Everett Hildenbrandt, Illinois]

standard

structured around formalisation

adding prose rendering for the uninitiated

webassembly.github.io/spec/

proposal process

must include spec text

must include formalisation!

must include OCaml reference interpreter

must include comprehensive test suite

must be implemented in 2 production engines

road map

v1 (shipped): support **low-level** languages

v2 (next+ year): support **high-level** languages

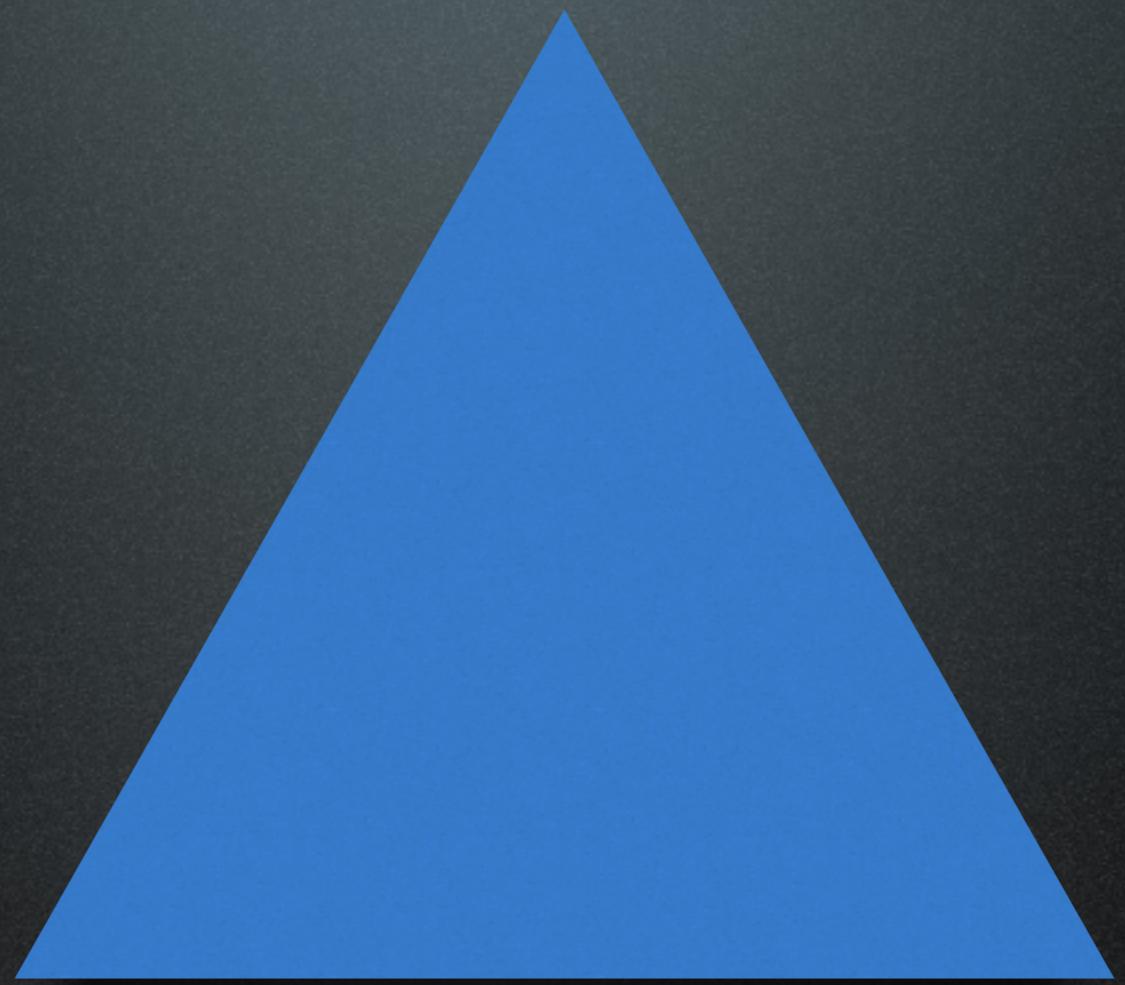
v3 (maybe...): support “**dynamic**” languages

future features

- tail calls
- references
- threads
- vector instructions
- exception handling
- coroutines / effect handlers
- garbage collection
- ...

<https://github.com/WebAssembly/proposals>

Performance



Simplicity

Generality

Performance

Safety

Simplicity

Generality

inside job

backing from key players

patience & stubbornness

adjust to audience

stay realistic

bus factor



Summary

Formal rigour and machine verification
in the mainstream

Led to a clean and simple design

Progress is slow and brittle

But there is hope