

# Verified trustworthy software systems

Monday 4 – Tuesday 5 April 2016

Royal Society discussion meeting organised by

Professor Philippa Gardner, Professor Peter O'Hearn, Professor Mike Gordon FRS, Professor Greg Morrisett and Professor Fred Schneider

THE  
ROYAL  
SOCIETY

| DAY 1  |   |  |  | DAY 2   |   |  |  |
|--|---|--|--|---|---|--|--|
| <b>Session 1</b><br><b>Chair : John Launchbury</b> |   | <b>Session 2</b><br><b>Chair : Warren Hunt</b> |  | <b>Session 3</b><br><b>Chair : Gordon Plotkin</b> |   | <b>Session 4</b><br><b>Chair : David Sands</b> |  |
| <b>09:00</b>                                       | Welcome by Lesley Miles and Sir Tony Hoare FRS                                      |  |  |   |   |  |  |
| <b>09:10</b>                                       | <b>J. Strother Moore</b><br>Industrial hardware and software verification with ACL2 | <b>13:35</b>                                   | <b>Daniel Kroening</b><br>Program analysis using syntax-guided synthesis engines         | <b>09:00</b>                                      | <b>Peter O'Hearn</b><br>Moving fast with software verification  | <b>13:30</b>                                   | <b>Michael Backes</b><br>imPACT: Privacy, Accountability, Compliance and Trust in Tomorrow's Internet.       |
| <b>09:45</b>                                       | <b>Cédric Fournet</b><br>miTLS: Verified Reference Implementations for TLS.         | <b>14:10</b>                                   | <b>Gerwin Klein</b><br>Provably trustworthy systems                                      | <b>09:35</b>                                      | <b>Philippa Gardner</b><br>Understanding and verifying JavaScript programs  | <b>14:05</b>                                   | <b>Alexey Gotsman</b><br>A rigorous approach to consistency in cloud databases                               |
| <b>10:20</b>                                       | Coffee  | <b>14:45</b>                                   | Tea  | <b>10:10</b>                                      | Coffee  | <b>14:40</b>                                   | Tea  |
| <b>10:45</b>                                       | <b>Kathleen Fisher</b><br>Using formal methods to eliminate exploitable bugs        | <b>15:10</b>                                   | <b>Nickolai Zeldovich</b><br>Using Crash Hoare logic for certifying the FSCQ file system | <b>10:40</b>                                      | <b>Fred B. Schneider</b><br>Avoiding fatal flaws with formal methods  | <b>15:10</b>                                   | <b>Xavier Leroy</b><br>Trust in programming tools: the formal verification of compilers and static analysers |
| <b>11:20</b>                                       | <b>Neil White</b><br>Formal verification: will the seedling ever flower?            | <b>15:45</b>                                   | <b>Mark Batty</b><br>Industrial concurrency specification for C/C++ & Nvidia GPUs        | <b>11:15</b>                                      | <b>Marco Pistoia</b><br>Combining static analysis and machine learning for industrial-quality information-flow-security enforcement | <b>15:45</b>                                   | <b>Greg Morrisett</b><br>Mind the gap: from crypto to code   |
| <b>11:55</b>                                       | <b>Discussion:</b><br>Verification in Industry                                      | <b>16:20</b>                                   | <b>Discussion:</b><br>Verification in Academia   | <b>11:50</b>                                      | <b>Discussion:</b><br>Verification in the Future  | <b>16:20</b>                                   | <b>Discussion:</b><br>What next for Verification?  |
| <b>12:35</b>                                       | LUNCH   | <b>17:00</b>                                   | CLOSE AND RECEPTION  | <b>12:30</b>                                      | LUNCH   | <b>17:00</b>                                   | CLOSE  |