

Holistic benchmarking in 3D Robot Vision: the SLAMBench open source framework

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In collaboration with:

B. Bodin, M Z. Zia, J. Mawer, E. Vespa, A. Nisbet, G. S. Shenoy, M. K. Emani, M. F. P. O'Boyle,
P. H. J. Kelly, B. Franke, M. Luján, A. J. Davison, G. Riley, N. Topham and S. Furber



The University of Manchester

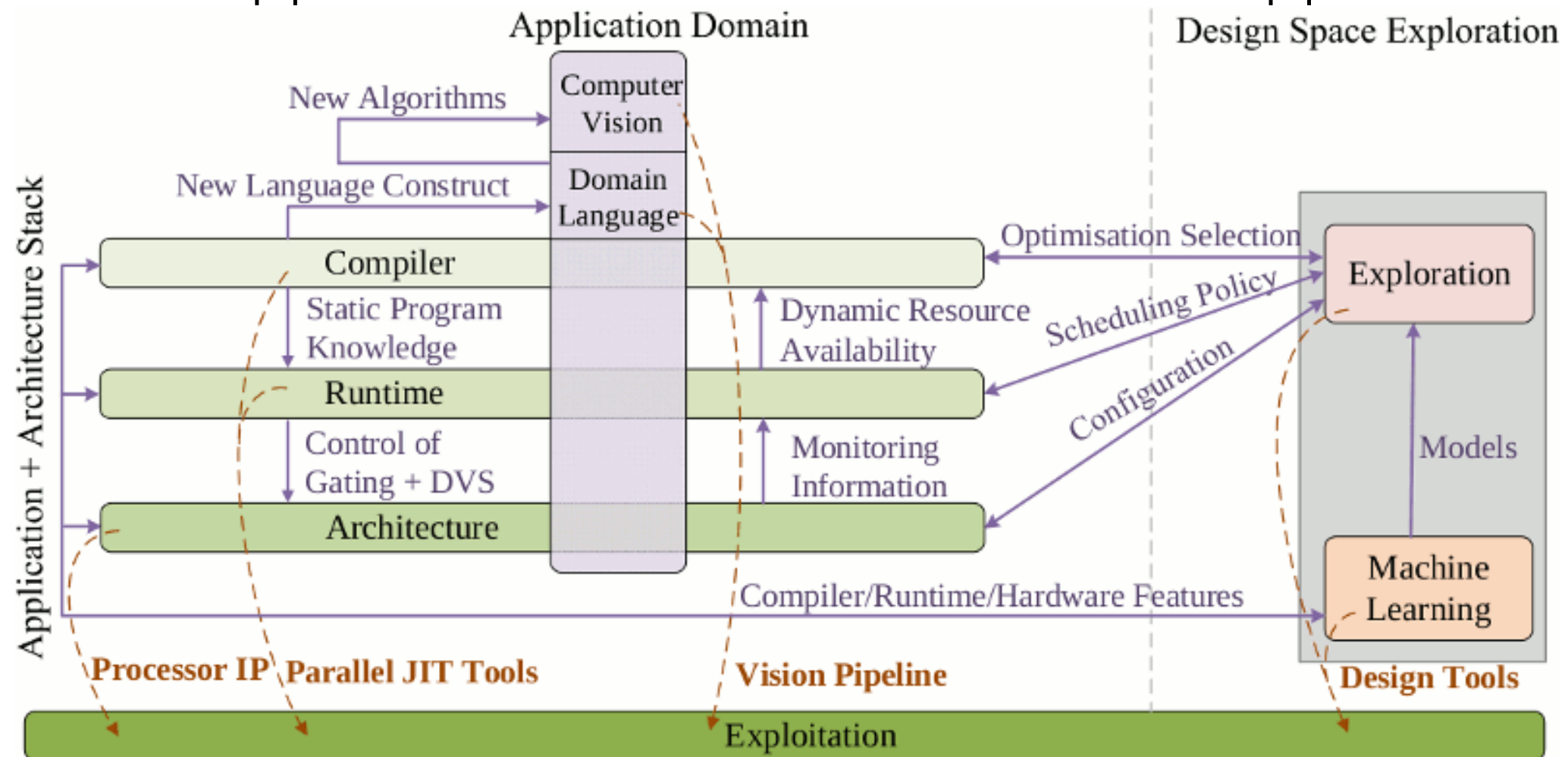


Imperial College
London



PAMELA project

Panoramic Approach to the Many-core Landscape -
from application to end-device: a holistic approach



In collaboration with:

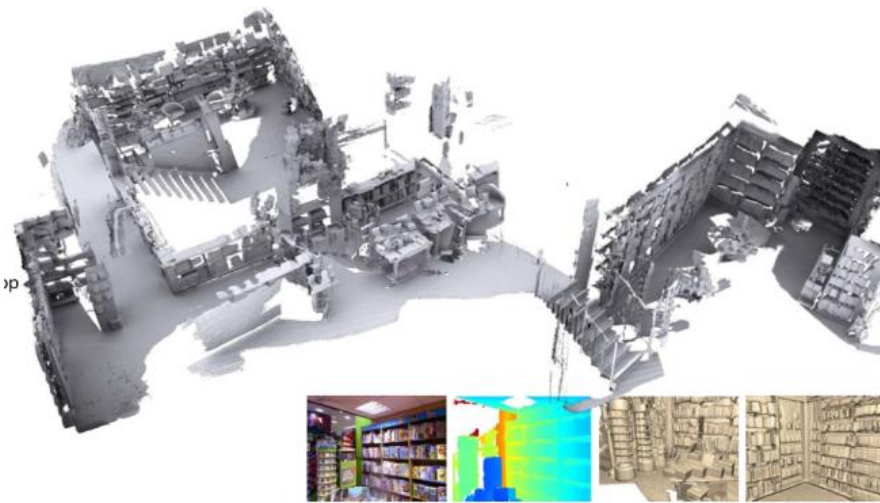
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The three R's of vision: Spectrum of Computer Vision Research

Reconstruction

Scalable Kinect Fusion
(2013)

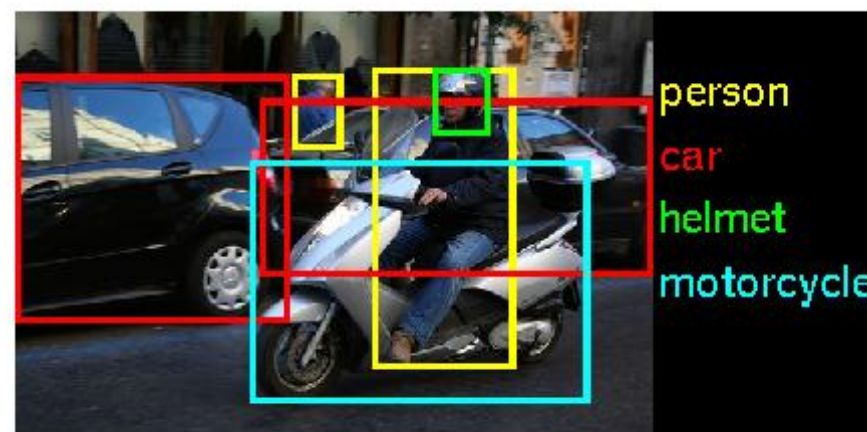
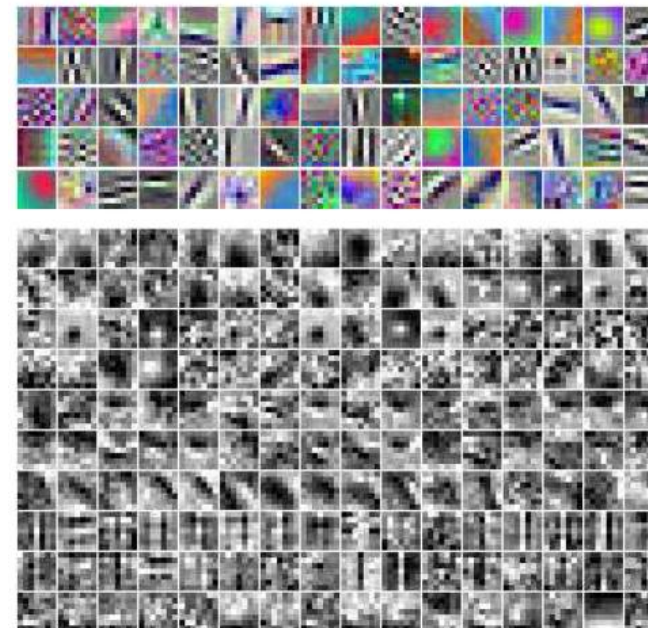


Building Rome on a
cloudless day (2010)



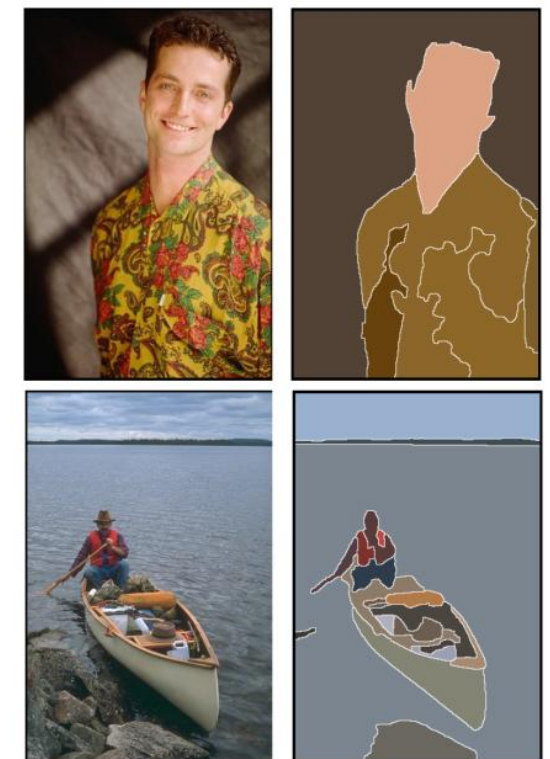
Recognition

Deep learning for scalable
Object class detection (2014)



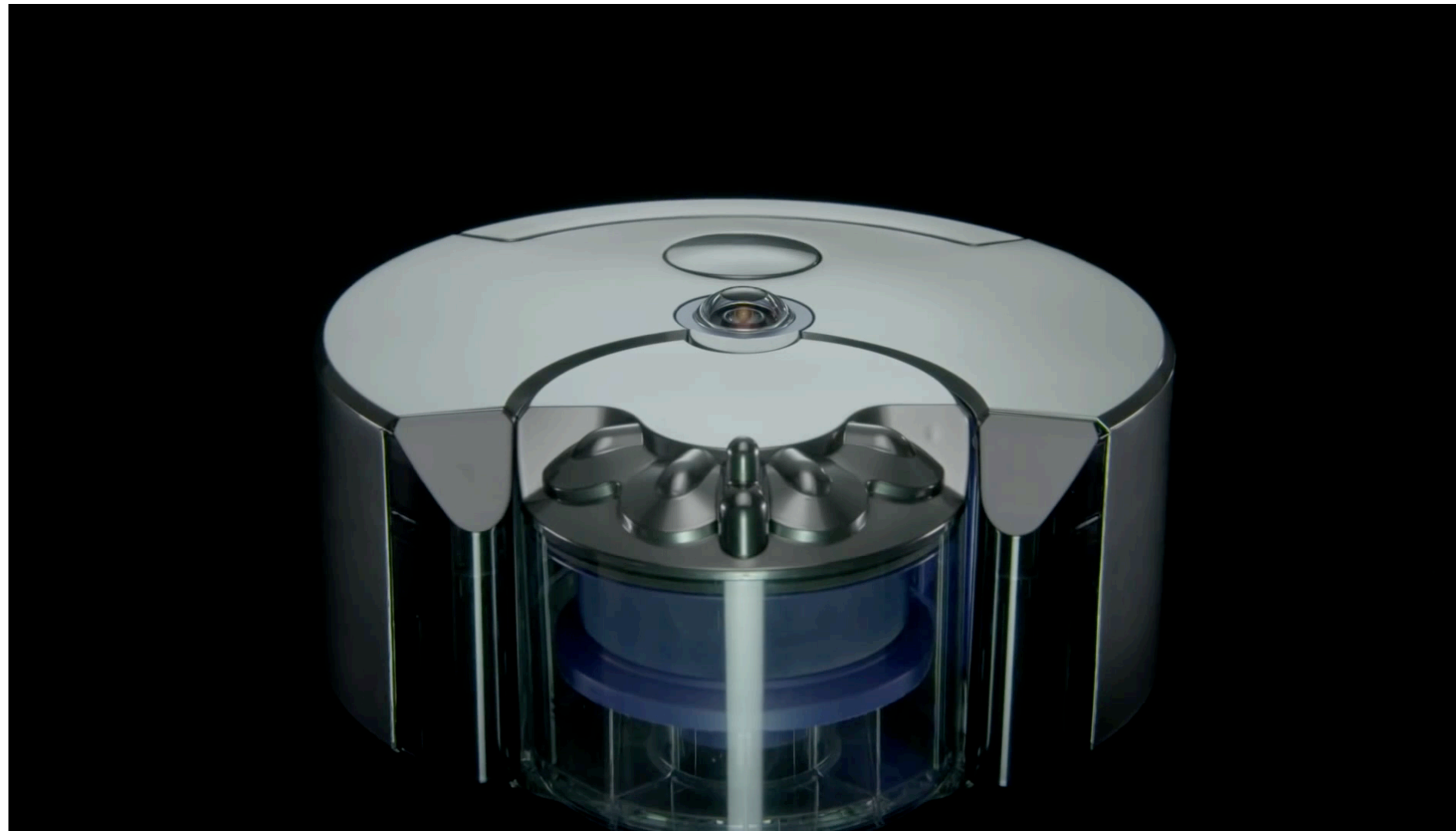
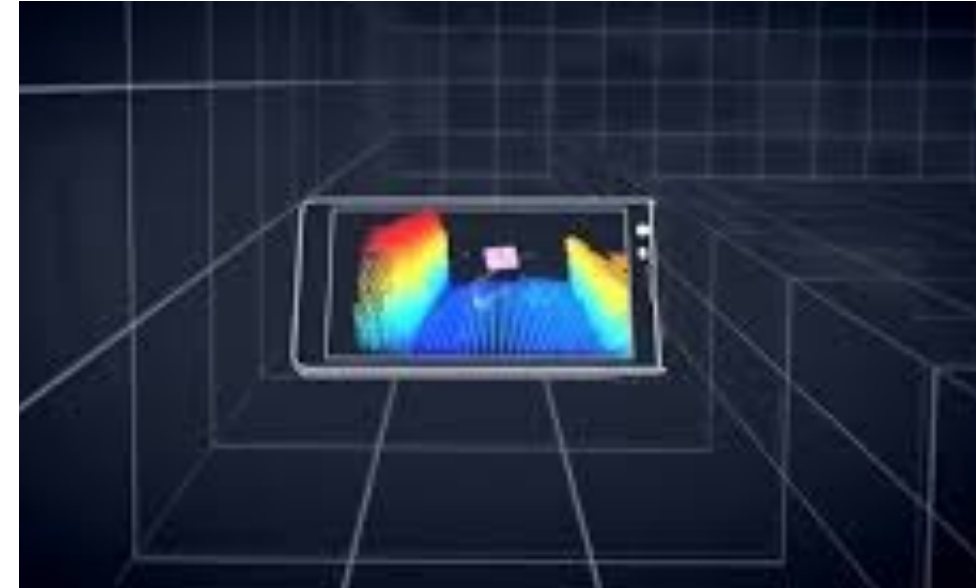
Reorganisation or Grouping

Contour detection
and segmentation (2011)



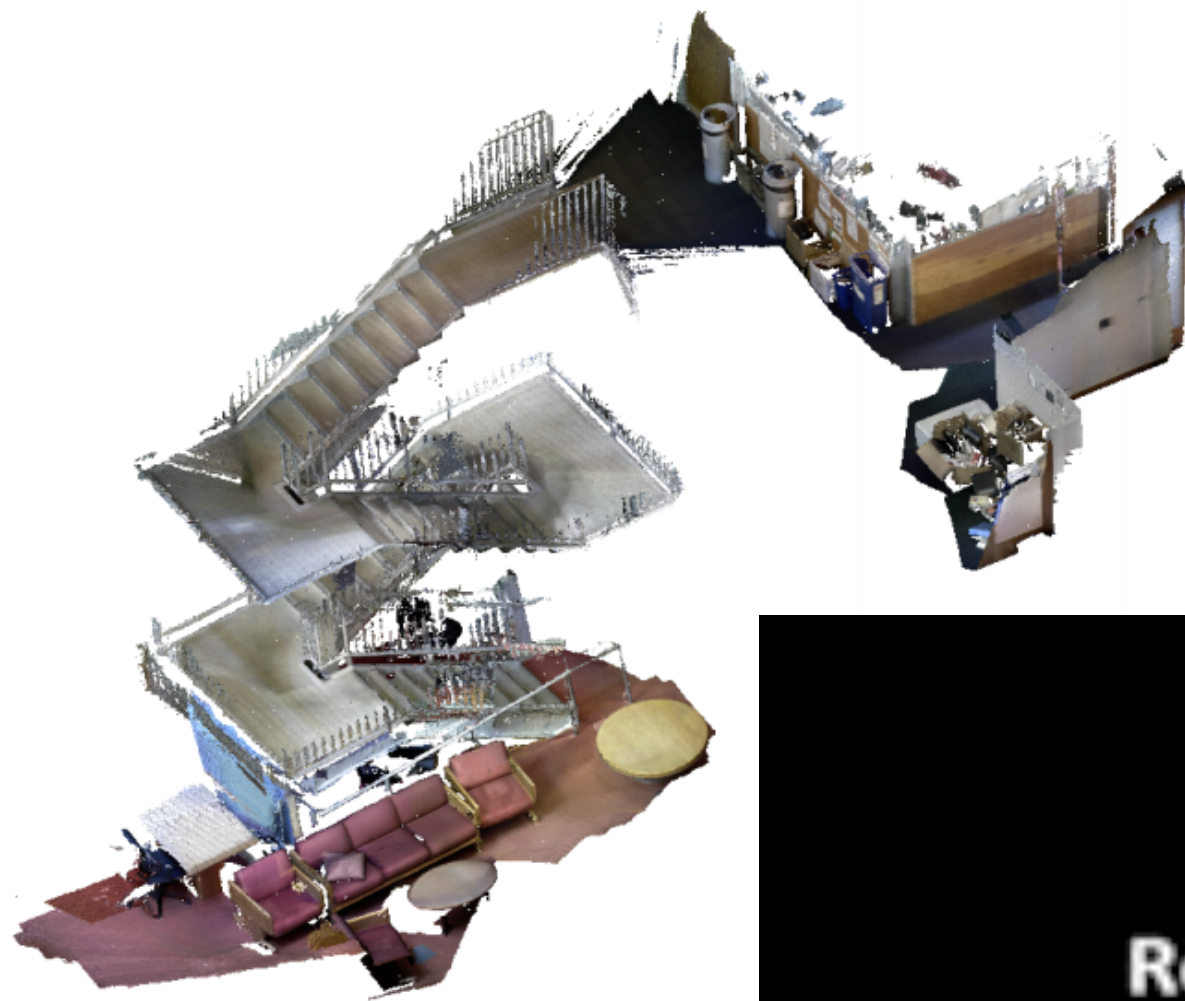
Simultaneous localisation and mapping (SLAM)

Build a coherent world representation and localise the camera in real-time

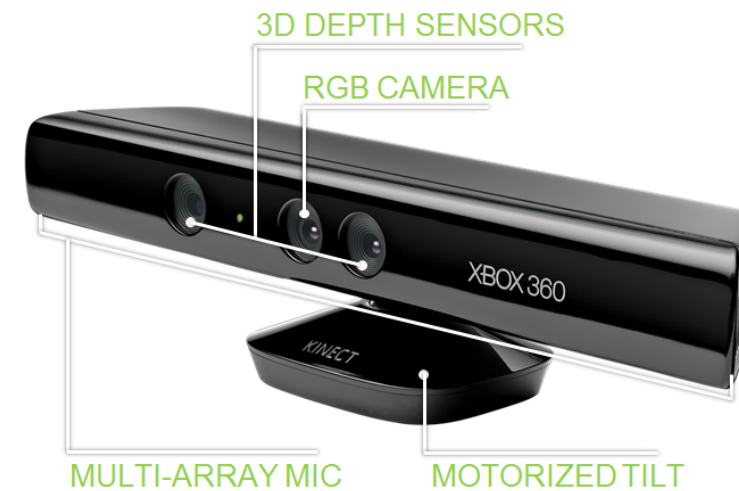


Video:
[Dyson 360 Eye](#)

Simultaneous localisation and mapping (SLAM)



[Whelan et al. 2012]



SIGGRAPH Talks 2011 KinectFusion: Real-Time Dynamic 3D Surface Reconstruction and Interaction

Shahram Izadi 1, Richard Newcombe 2, David Kim 1,3, Otmar Hilliges 1,
David Molyneaux 1,4, Pushmeet Kohli 1, Jamie Shotton 1,
Steve Hodges 1, Dustin Freeman 5, Andrew Davison 2, Andrew Fitzgibbon 1

1 Microsoft Research Cambridge 2 Imperial College London
3 Newcastle University 4 Lancaster University
5 University of Toronto

Video:

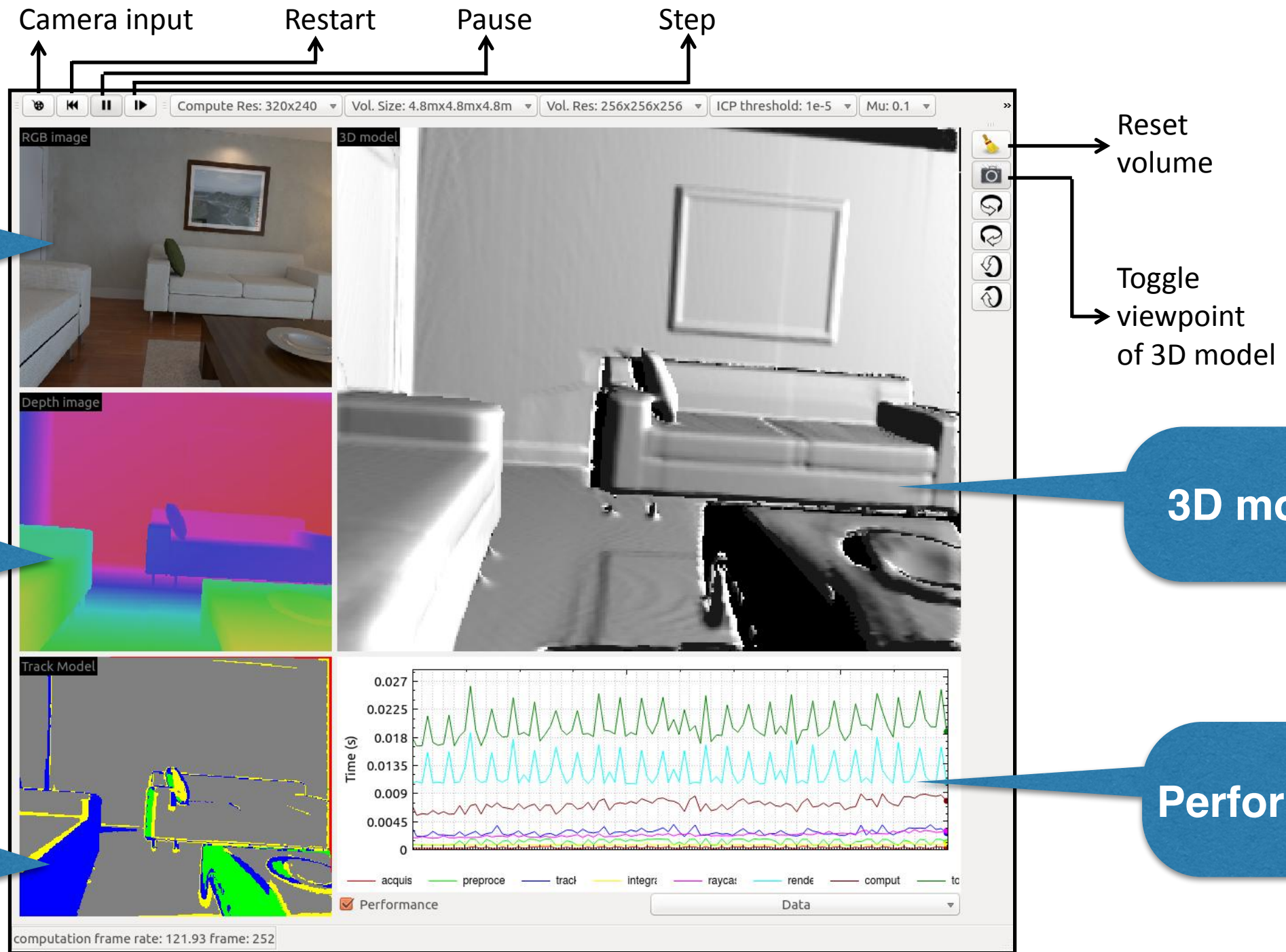
[Newcombe et al. ISMAR 2011]

Demo: 3D reconstruction of Albert

This slide has been added after the presentation and shows the live demonstration of the SLAMBench framework performed (see next slide for explanation)



SLAMBench GUI



What is “Performance”?

1. In several domains performance is **execution time**
2. In some domains performance is **accuracy**
3. What about **energy**?
4. But also memory consumption, temperature, robustness, etc.

A modern system evaluation considers multiple metrics:

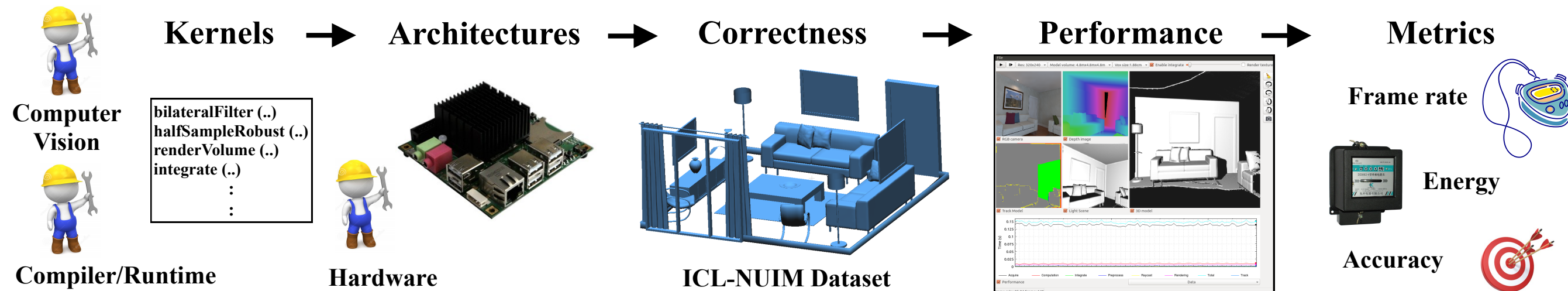
$$Performance = \begin{bmatrix} Runtime \\ Energy \\ Accuracy \end{bmatrix}$$

This defines a multi-objective optimisation problem: trade-off

Three “Performance” metrics

Holistic approach to SLAM “performance”:

SLAMBench



A publicly-available benchmarking framework for quantitative, comparable and validatable experimental research to investigate trade-offs in performance, accuracy and energy consumption of a SLAM system



How to measure SLAM “Performance”?

SLAM computation depends on:

- Images acquired
- Way the camera is moved
- Numerical approximations
- Processing frame rate
(depends on hardware capability)

**Need for reproducibility
and accuracy check**

Pre-recorded scenes

Process-every-frame mode

*ICL-NUIM and
TUM RGB-D datasets*

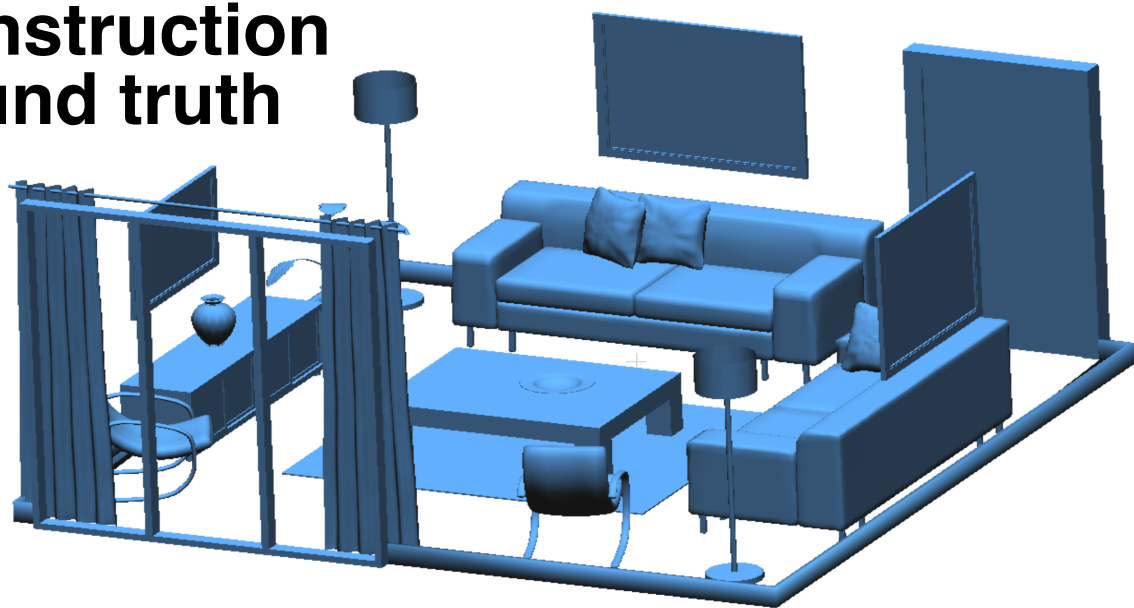


ICL-NUIM dataset

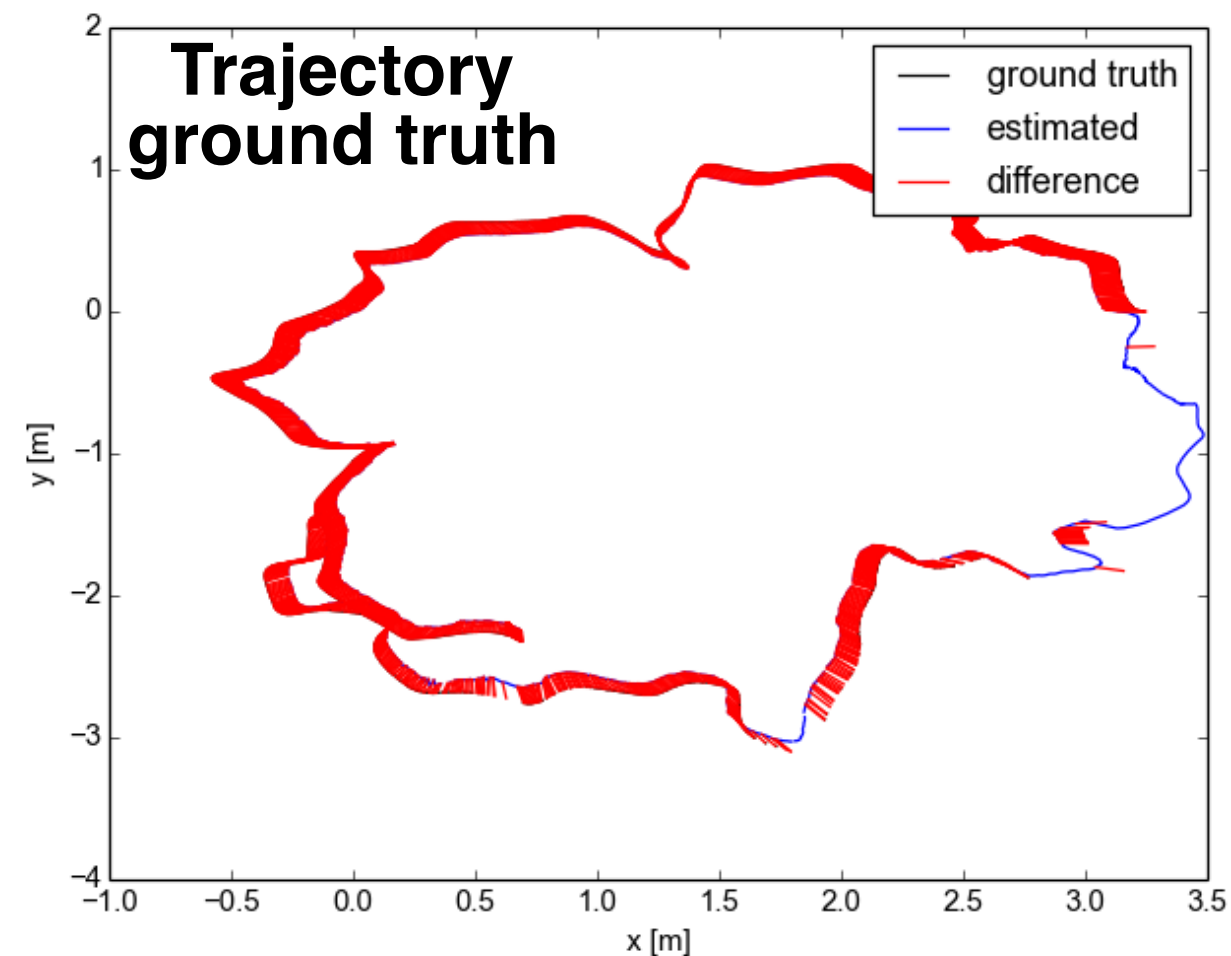
Luigi Nardi - Imperial College London



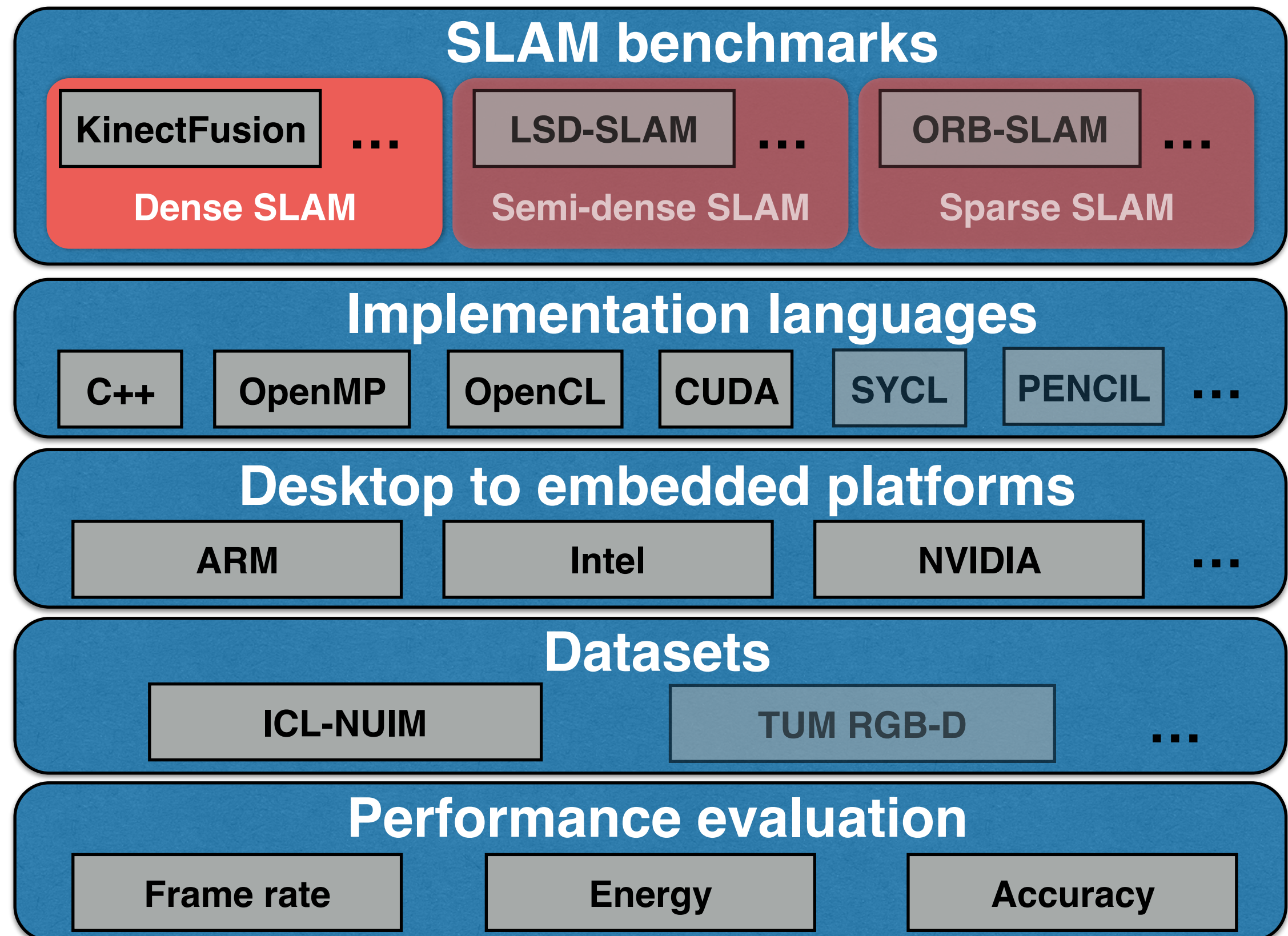
Reconstruction ground truth



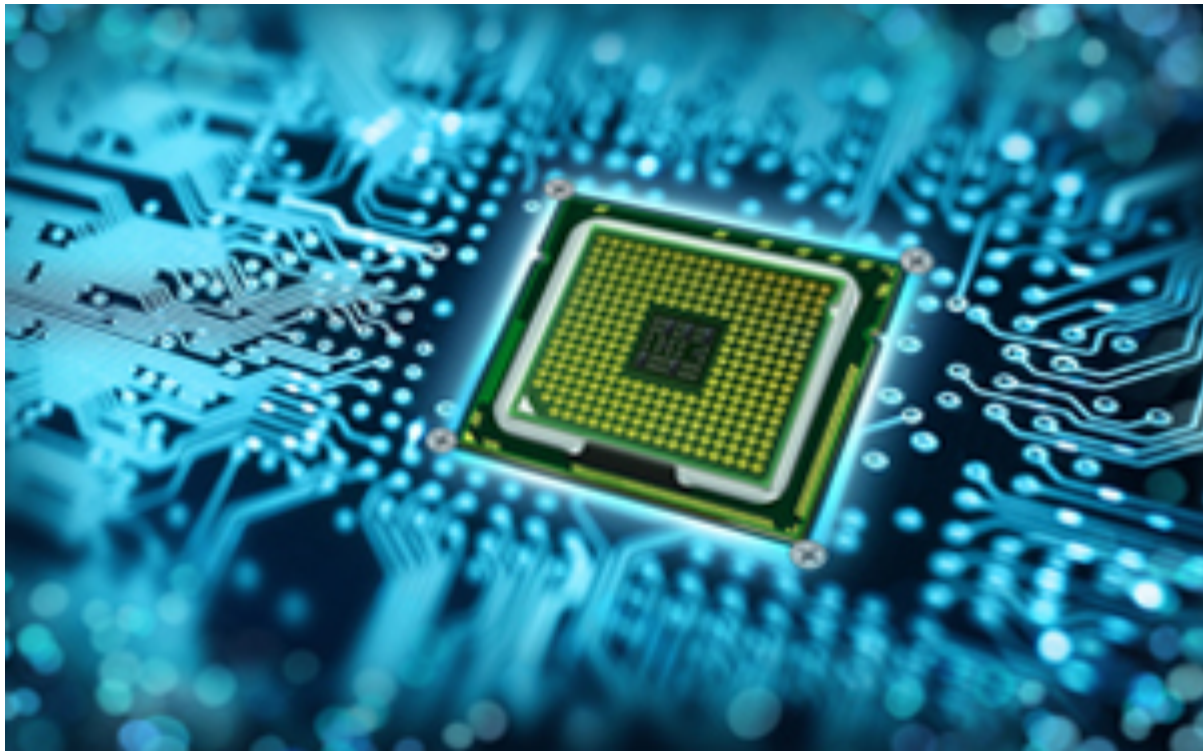
- ICL-NUIM synthetic dataset [Handa et al. 2014]
- 880 RGB-D frames at 30 FPS
- Absolute trajectory error (ATE) based on ground truth



SLAMBench framework



SLAMBench opportunities



Chip design and simulation tools



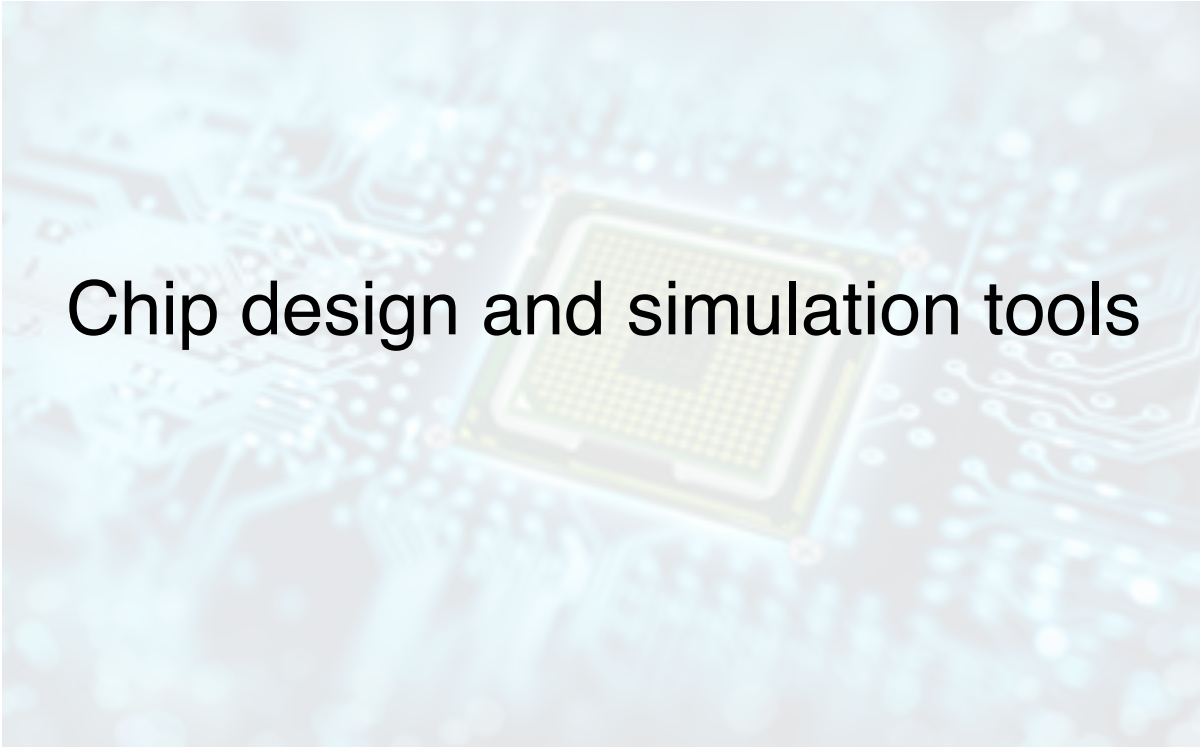
SLAMBench opportunities



Chip design and simulation tools



SLAMBench opportunities



Chip design and simulation tools



SLAMBench evolution:

- Point fusion
- Octrees
- Semi-dense SLAM
- Feature-based SLAM

Kernels can be improved individually



SLAMBench opportunities



Chip design and simulation tools

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Kernels can be improved individually

Domain-specific language (DSL) targeting high performance, low-power solutions

Design-space exploration, e.g. algorithmic, compiler and hardware parameters

SLAMBench kernels tuning, e.g. vectorisation, GPU, auto-tuning



SLAMBench opportunities



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SLAMBench evolution:

- Point fusion
- Octrees
- Semi-dense SLAM
- Feature-based SLAM

Kernels can be improved individually

- CPU/GPU mapping/partitioning
- Just-in-time compilation



SLAMBench today

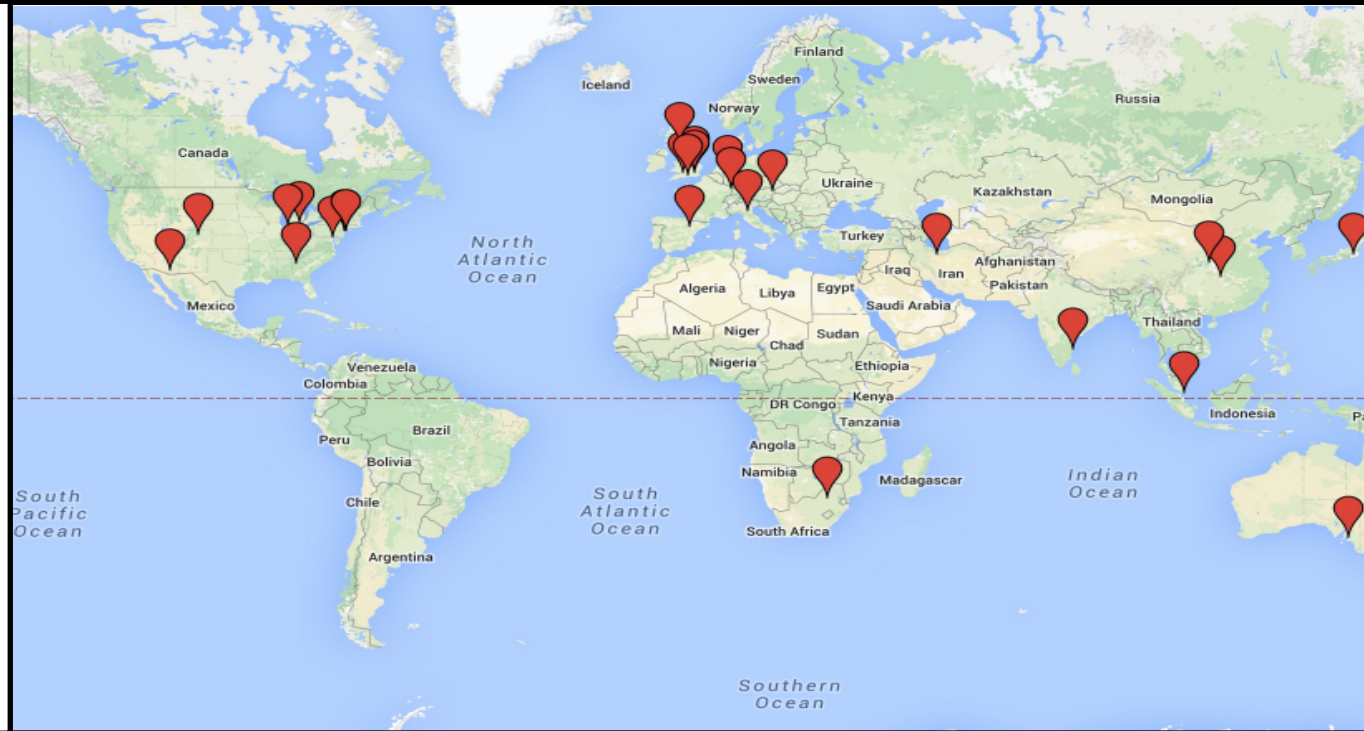
- Publicly released
13/11/2014
(1200+ downloads)

- Early adopters:
 - Computer Vision
 - Compiler/runtime
 - Architecture

Company users



Academic users



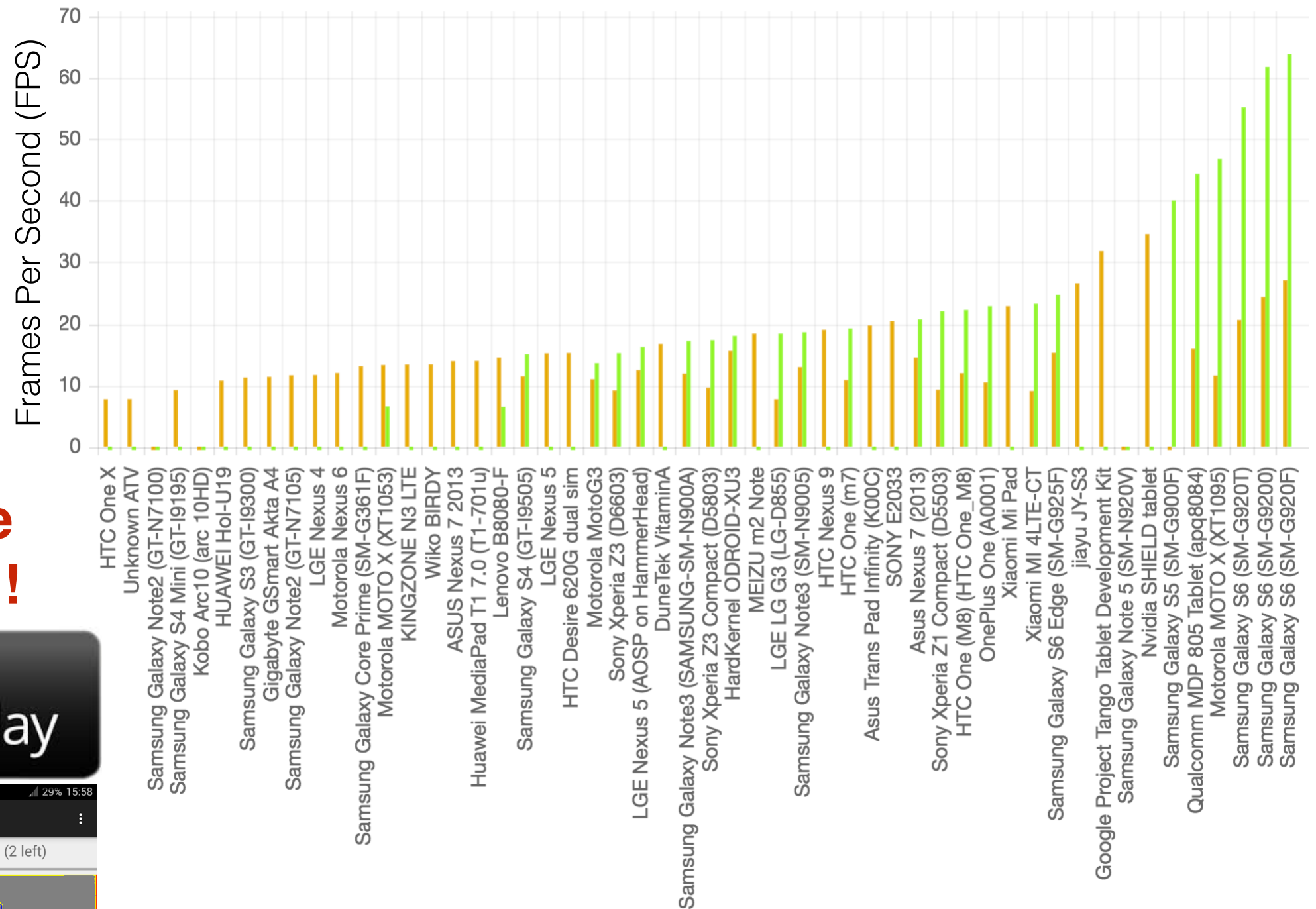
Web: apt.cs.manchester.ac.uk/projects/PAMELA/tools/SLAMBench/

- Introducing SLAMBench, a performance and accuracy benchmarking methodology for SLAM (ICRA 2015)*
 - Comparative Design Space Exploration of Dense and Semi-Dense SLAM (ICRA 2016)*
- Papers for further reads**

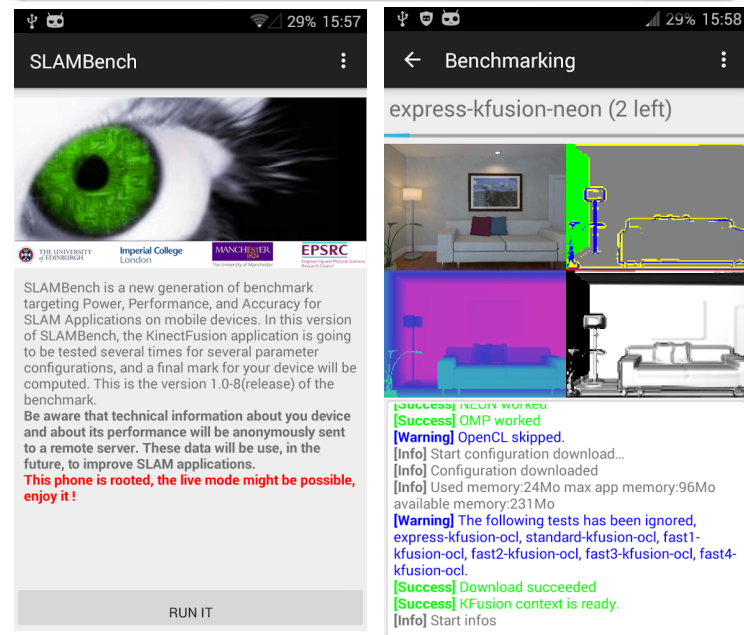
Crowdsourcing mobile Android SLAMBench

- SLAMBench OpenMP
- SLAMBench OpenCL

Get it now,
And see where
your device is!!



- It runs a set of configurations on the available languages on your device
- Then shows the best achieved result



What is the optimisation space?

Configuration parameters:

Space 1

1. Algorithmic:
 - Application-specific parameters
 - Minimisation methods
 - Early exit condition values

What is the optimisation space?

Configuration parameters:

Space 1	1. Algorithmic: • Application-specific parameters • Minimisation methods • Early exit condition values
Space 2	2. Compilation: • opencl-params: -cl-mad-enable,-cl-fast-relaxed-math, etc. • LLVM flags: O1, O2, O3, vectorize-slp-aggressive, etc. • Local work group size: 16/32/64/96/112/128/256 • Vectorisation: width (1/2/4/8), direction (x/y) • Thread coarsening: factor (1/2/4/8/16/32), stride (1/2/4/8/16/32), dimension (x/y)

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Space 3	3. Architecture: • GPU frequency: 177/266/350/420/480/543/600/DVFS • # of active big cores: 0/1/2/3/4 • # of active LITTLE cores: 1/2/3/4

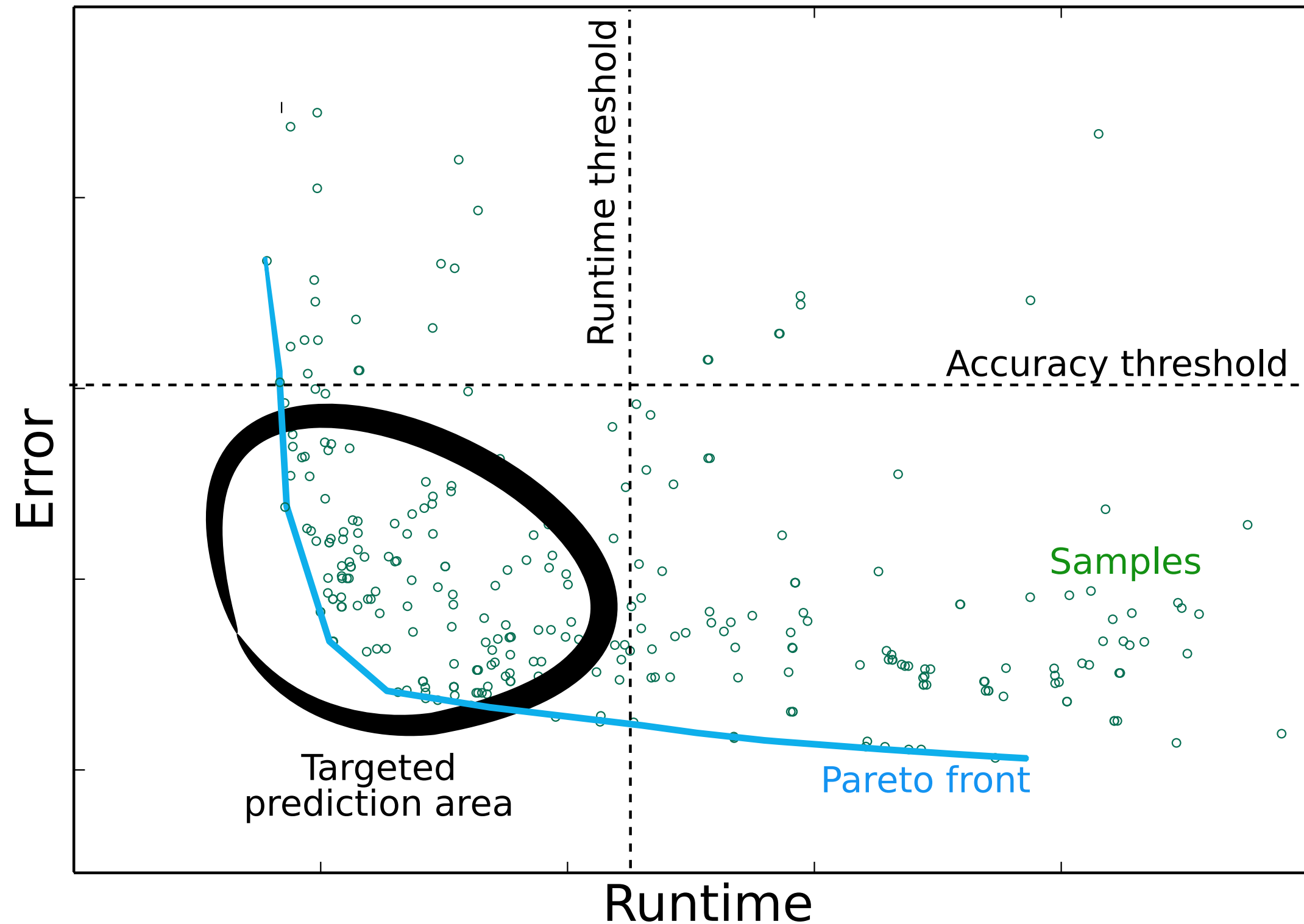
What is the optimisation space?

Configuration parameters:

Co-design space	Space 1	1. Algorithmic: • Application-specific parameters • Minimisation methods • Early exit condition values
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	Space 3	3. Architecture: • GPU frequency: 177/266/350/420/480/543/600/DVFS • # of active big cores: 0/1/2/3/4 • # of active LITTLE cores: 1/2/3/4

Warning: huge spaces, impossible to run exhaustively

Exploration goal



Algo design-space exploration (DSE)

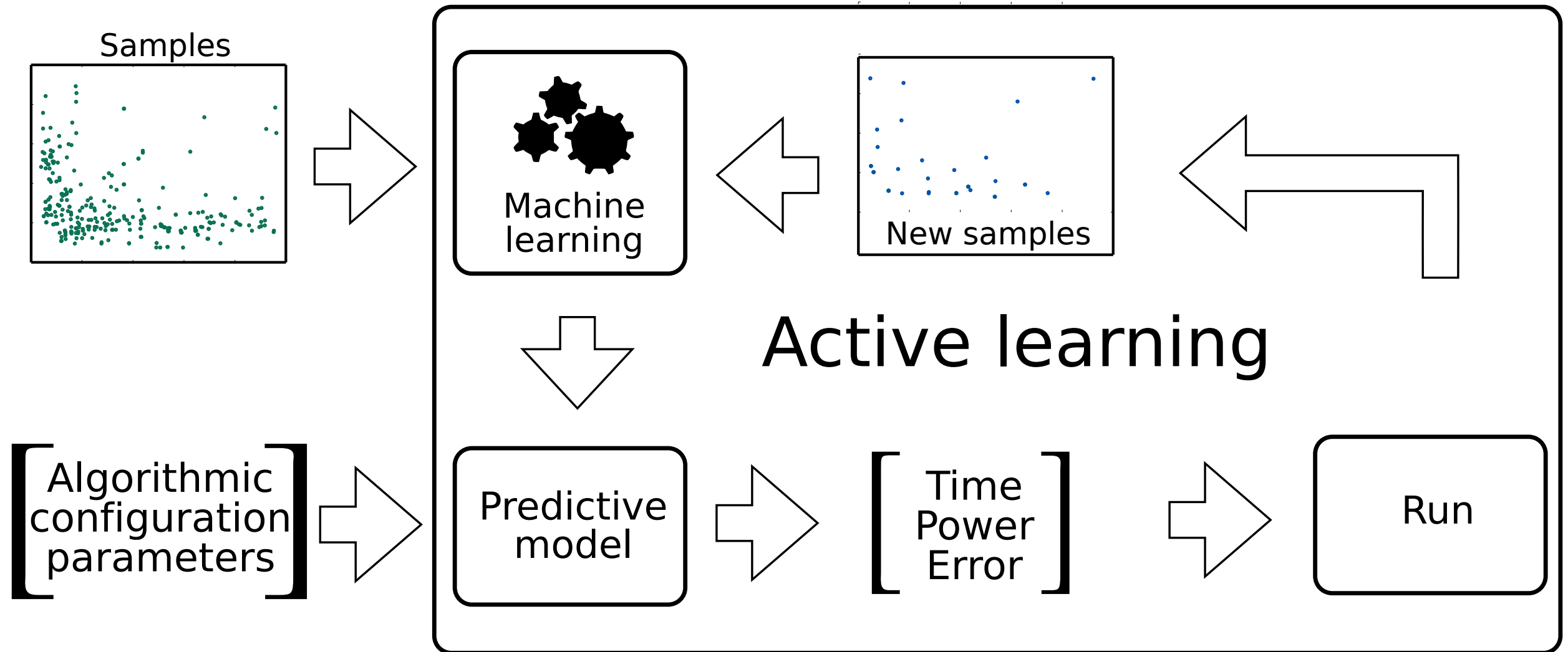


Figure 1 consists of two parts: (a) and (b).

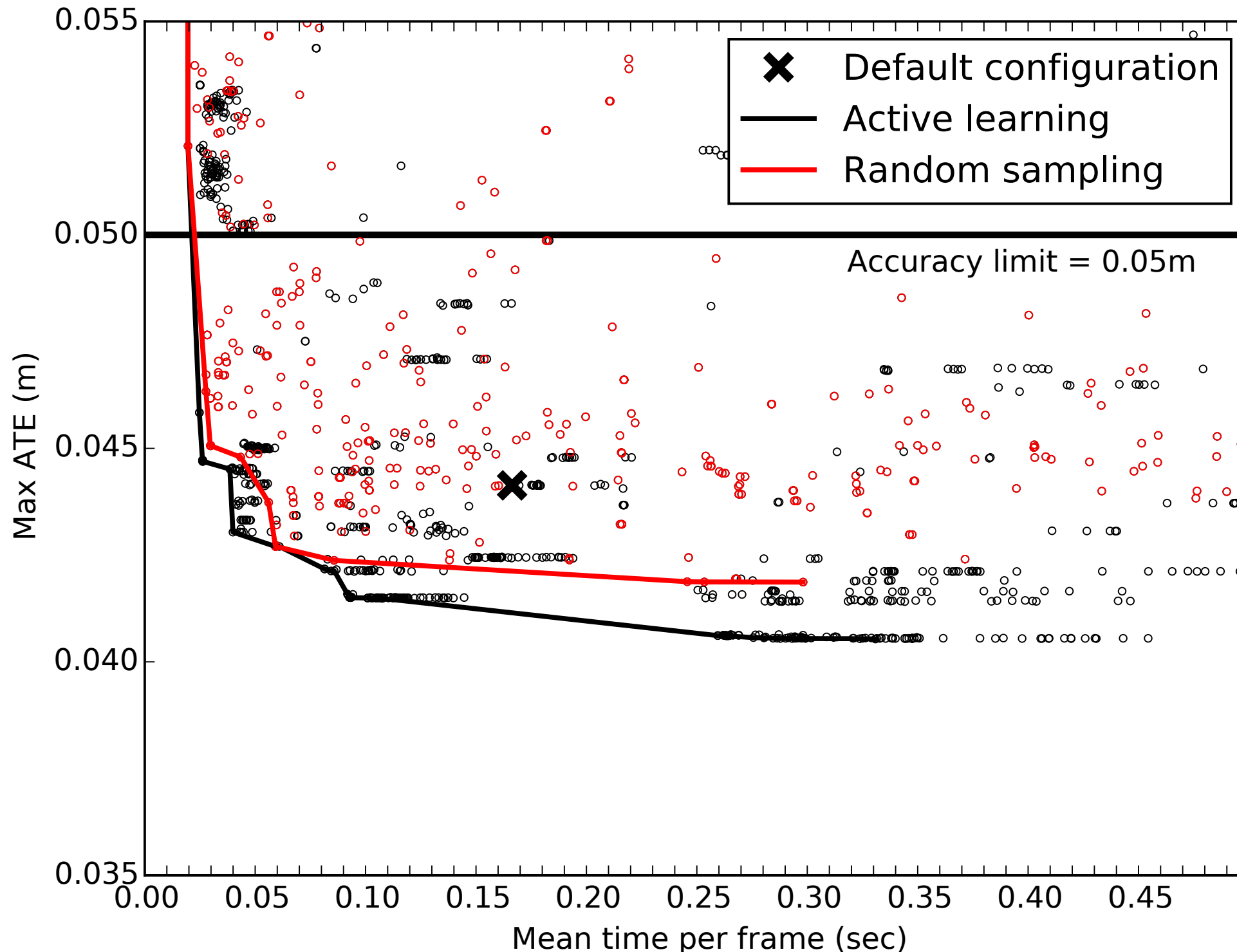
Part (a) shows a 2D plot with axes x_1 and x_2 . The space is partitioned into regions A, B, C, D, E, and F by thresholds $k_1, k_2, k_3, k_4,$ and k_5 . Red numbers represent class labels. The regions are defined as follows:

- Region A: $x_1 \leq k_5, x_2 \leq k_4$
- Region B: $x_1 \leq k_5, k_4 < x_2 \leq k_1$
- Region C: $k_5 < x_1 \leq k_2, x_2 \leq k_4$
- Region D: $k_5 < x_1 \leq k_2, k_4 < x_2 \leq k_1$
- Region E: $k_2 < x_1 \leq k_3, x_2 \leq k_4$
- Region F: $k_2 < x_1 \leq k_3, k_4 < x_2 \leq k_1$

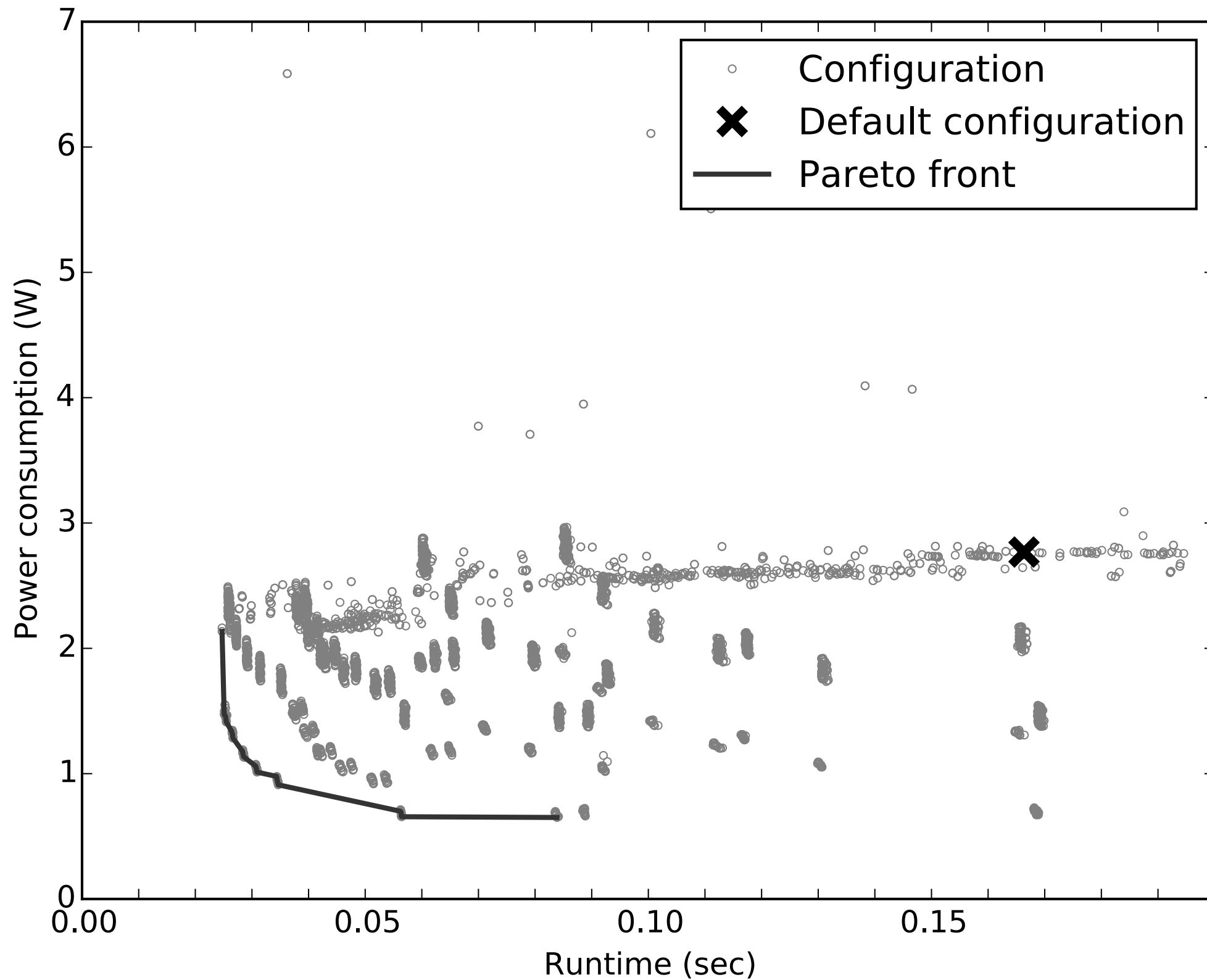
Part (b) shows a decision tree structure corresponding to the partitioning in (a). The root node splits on $x_2 > k_1$. The left branch splits on $x_1 > k_2$, leading to nodes C and D. The right branch splits on $x_1 > k_3$, leading to nodes E and F. Node C splits on $x_2 > k_4$, leading to nodes A and B. Node D splits on $x_1 > k_5$, leading to nodes A and B. Each node is associated with a histogram showing the distribution of y for that region.

DSE on algorithmic parameters error/runtime

Machine	CPU	CPU name	CPU GFLOPS	CPU cores	GPU	GPU name	GPU GFLOPS	TDP Watts
Hardkernel ODROID-XU3	ARM A15 + A7	Exynos 5422	80	4 + 4	ARM	Mali-T628	60 + 30	10



DSE architecture parameters power/runtime



DSE final results

Constraint	Runtime (FPS)	Max ATE (cm)	Power (Watts)
Default	6.03	4.41	2.77
Best runtime	39.85	4.47	1.47
Best accuracy	1.51	3.30	2.38
Best power	11.92	4.45	0.65
Power < 1W	29.09	4.47	0.98
Power < 2W	39.85	4.47	1.47
FPS > 10	11.92	4.45	0.65
FPS > 20	28.87	4.47	0.91
FPS > 30	32.38	4.47	1.01

- Most of the improvement comes from the algorithmic space
- KinectFusion real-time on a popular embedded device
- Enabling auto-tuning at the domain-specific language (DSL) level

SLAMBench vs other benchmarking frameworks: should HPC follow this model?

- Multi-objective optimisation: frame rate/power/accuracy
- Semantic accuracy check is very powerful:
 - ✱ enables non bit-wise accuracy check
 - ✱ scope for aggressive approx. computing and auto-tuning
- Several datasets:
 - ✱ not just 1 "big" and 1 "small"
- Multi-kernel benchmark:
 - ✱ enables benchmarking complex frameworks
- Multiple applications from the same domain (SLAM)
 - ✱ this is coming ...

References I

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Backup slides



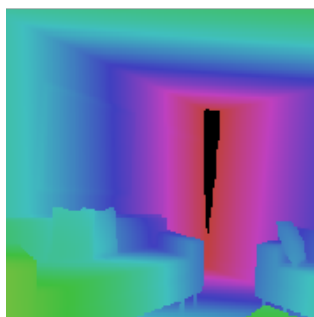
KinectFusion algorithmic features

Features	Ranges
Volume resolution	64x64x64, 128x128x128, 256x256x256, 512x512x512
μ distance	0 .. 0.5
Pyramid level iterations (3 levels)	0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11
Image resolution (image ratio)	1, 2, 4, 8
Tracking rate	1, 2, 3, 4, 5
ICP threshold	10^{-6} .. 10^2
Integration rate	1 .. 30

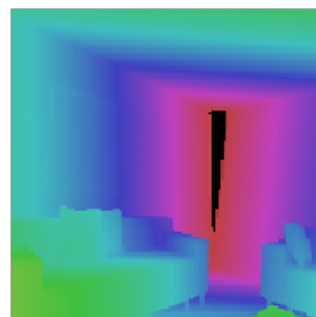
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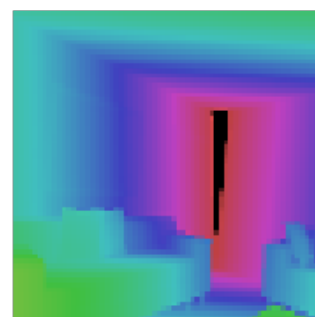
Image resolution (image ratio)



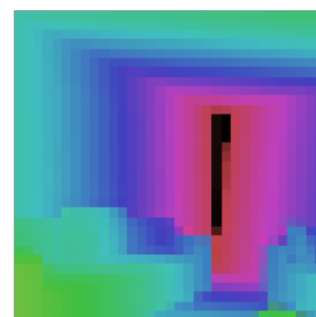
640x480



320x240



160x120

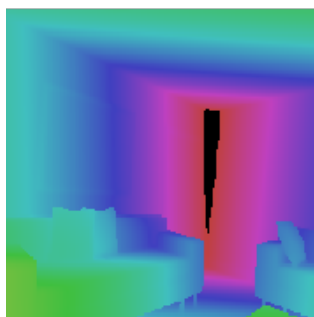


80x60

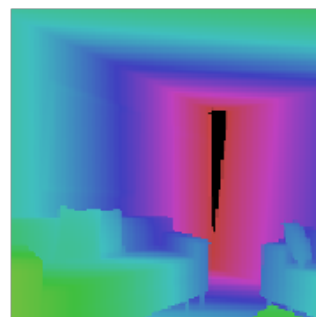
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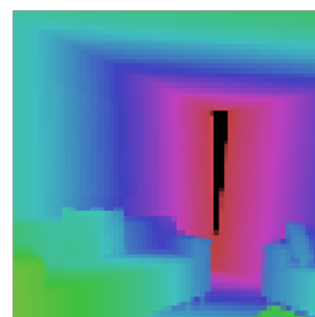
Image resolution (image ratio)



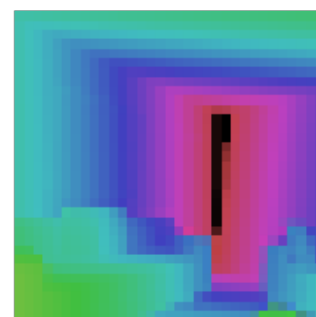
640x480



320x240

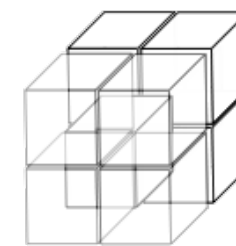


160x120

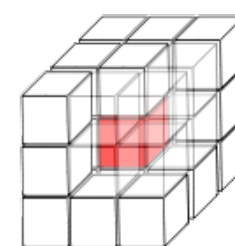


80x60

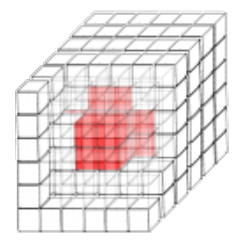
Volume resolution



2x2x2

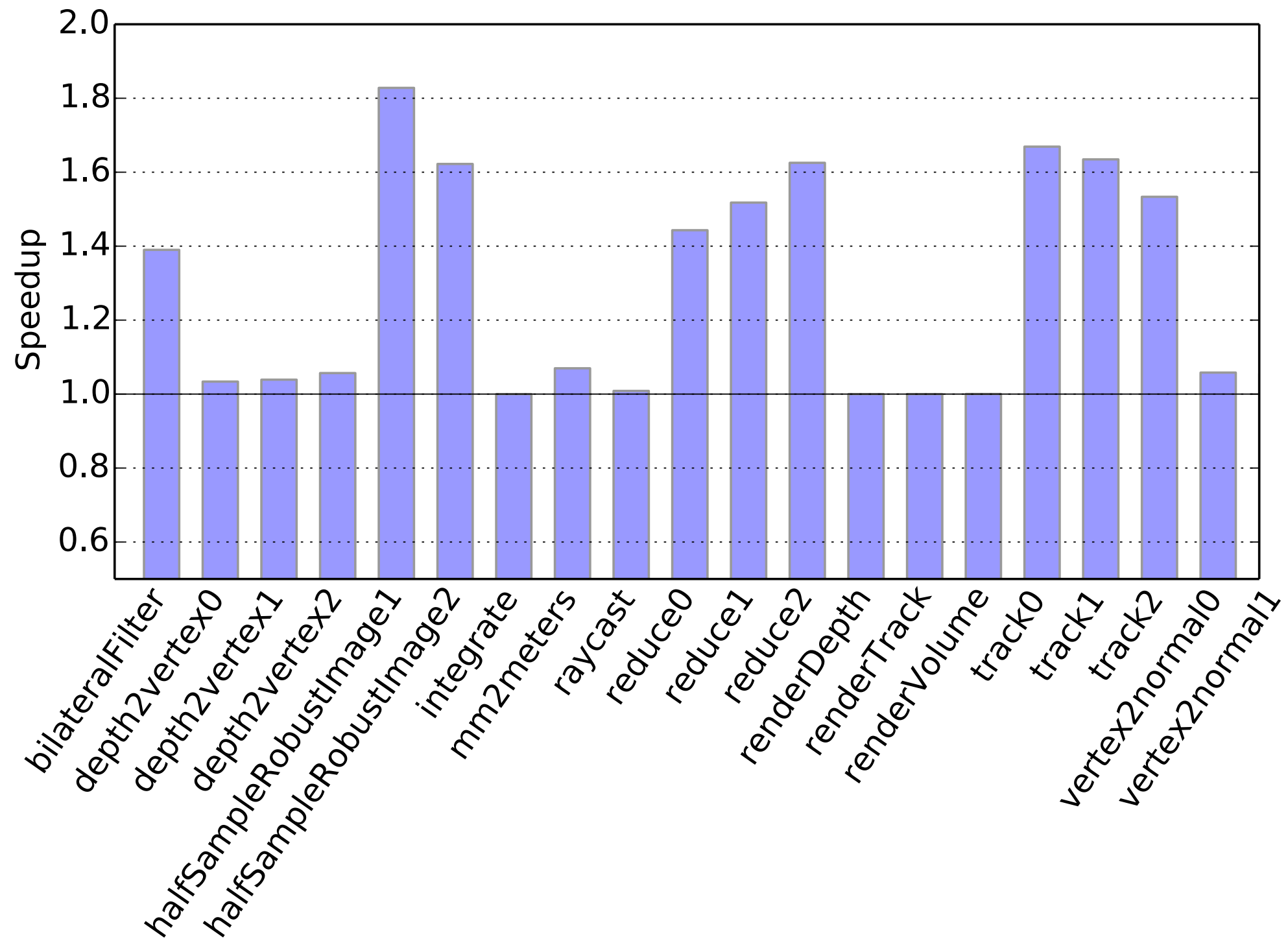


3x3x3



6x6x6

DSE compiler parameters speedup



“Performance” on SLAMBench

- Runtime/energy/accuracy measurements
- Accuracy provided via absolute trajectory error (ATE)



Machine	CPU	CPU name	CPU GFLOPS	CPU cores	GPU	GPU name	GPU GFLOPS	TDP Watts
Hardkernel ODROID-XU3	ARM A15 + A7	Exynos 5422	80	4 + 4	ARM	Mali-T628	60 + 30	10

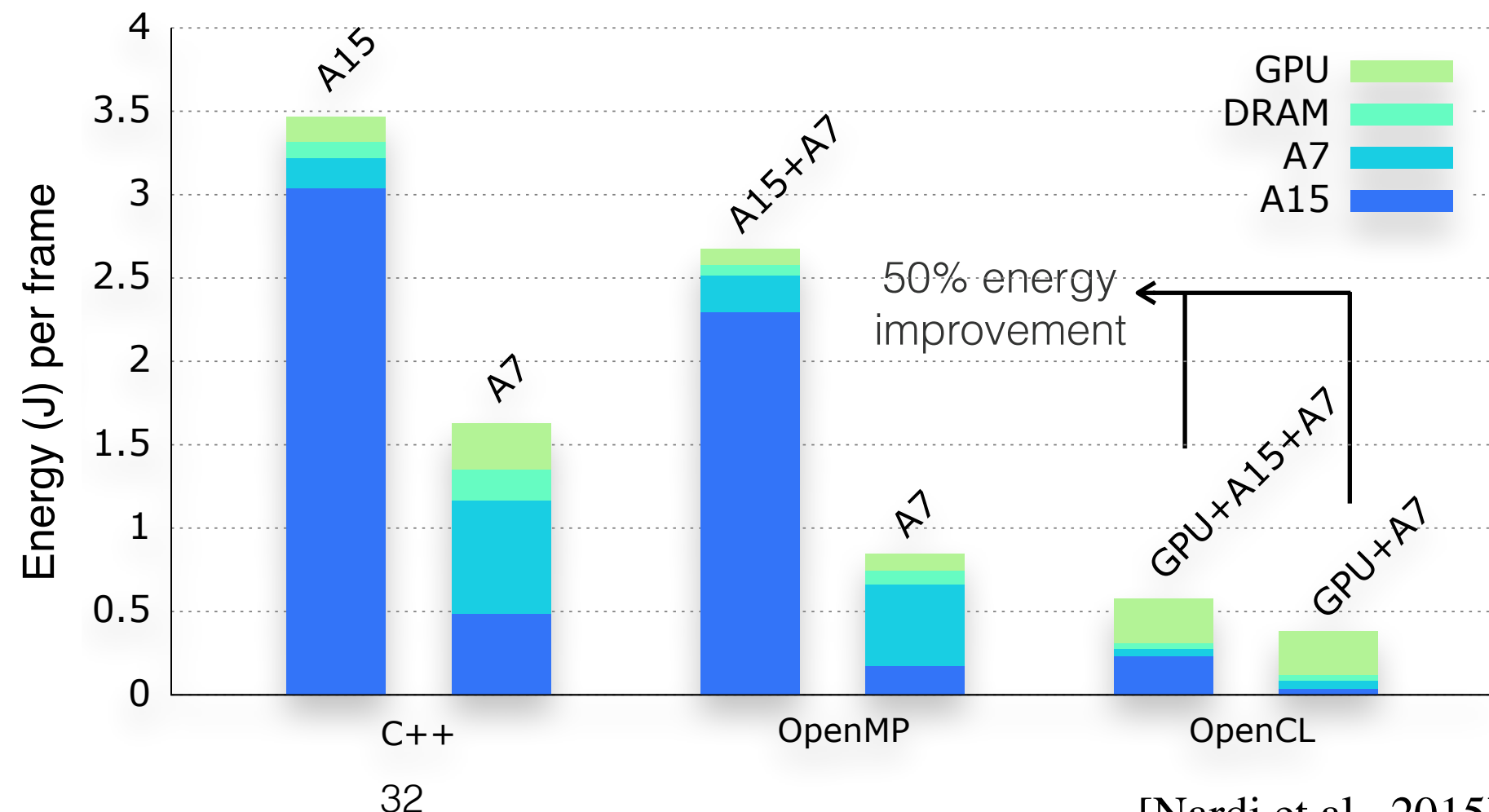
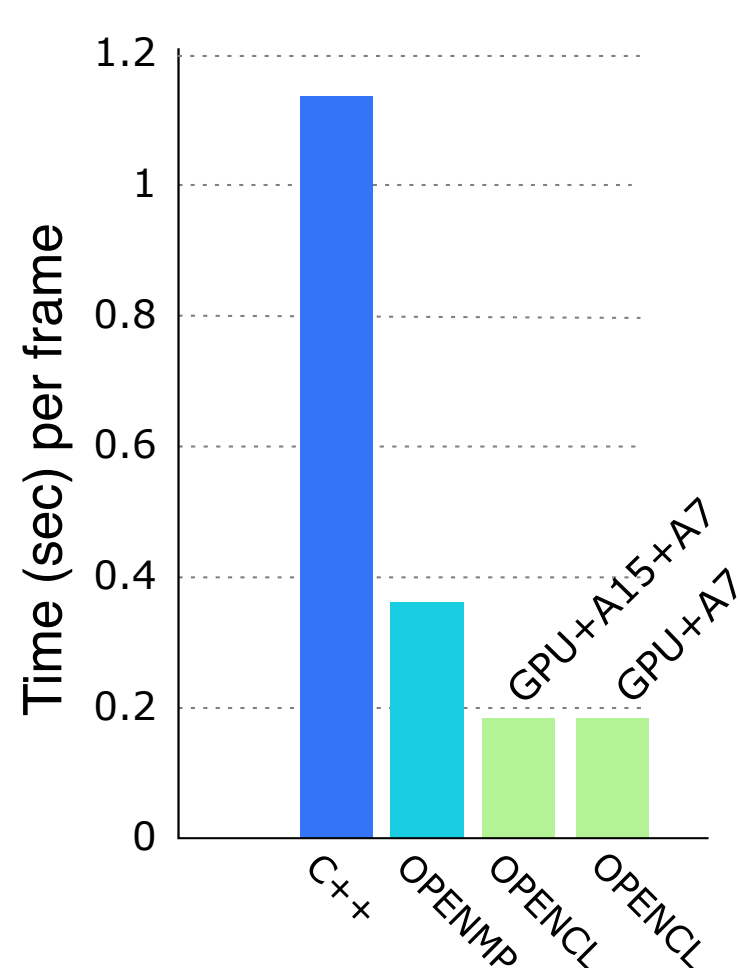
“Performance” on SLAMBench

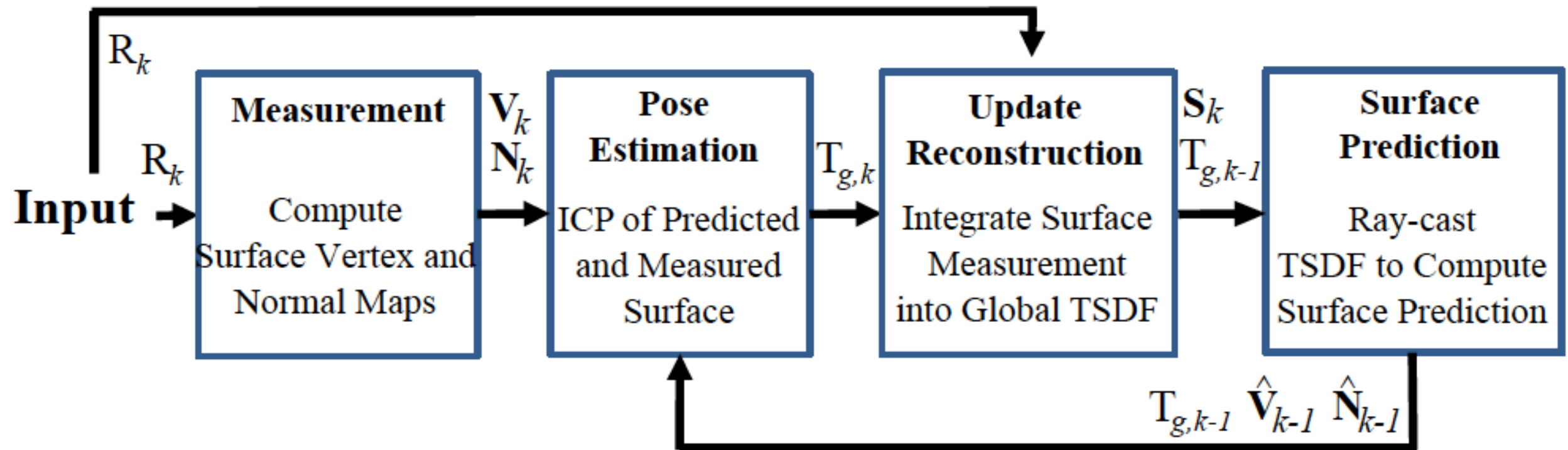
- Runtime/energy/accuracy measurements
- Accuracy provided via absolute trajectory error (ATE)



ATE in cm	
C++	2.06
OpenMP	2.06
OpenCL	2.01

Machine	CPU	CPU name	CPU GFLOPS	CPU cores	GPU	GPU name	GPU GFLOPS	TDP Watts
Hardkernel ODROID-XU3	ARM A15 + A7	Exynos 5422	80	4 + 4	ARM	Mali-T628	60 + 30	10



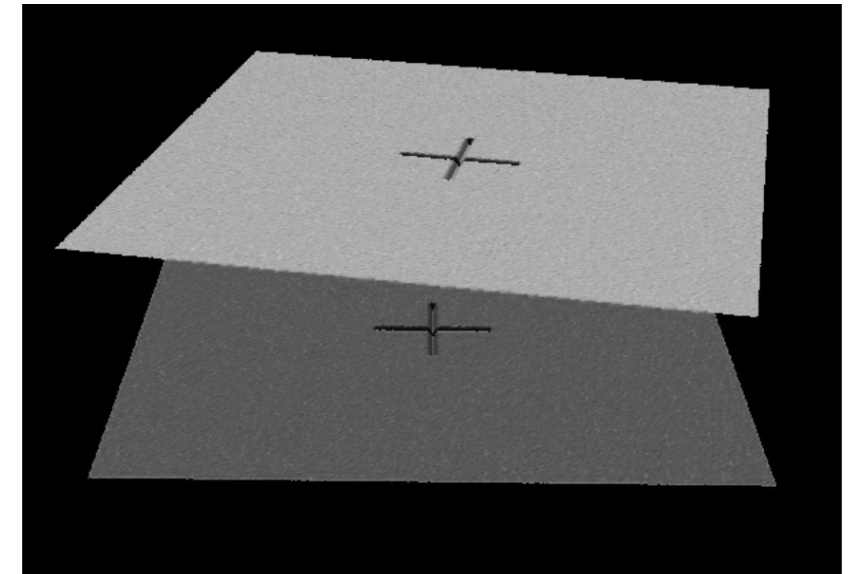
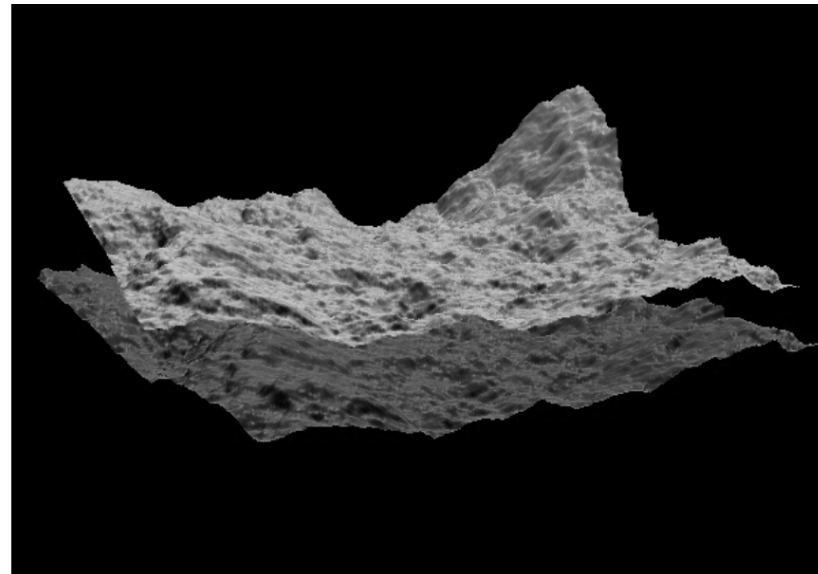
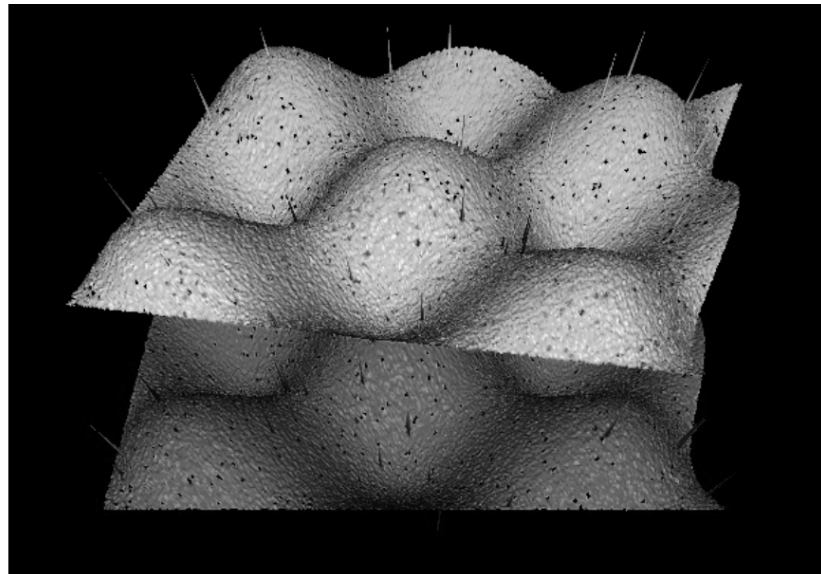


KinectFusion pipeline

- First dense monocular SLAM algorithm [Newcombe et al. ISMAR 2011]
- Adopted as a major building block in more recent SLAM systems
- Implementation based on [Reitmayr 2011] CUDA implementation



ICP registration

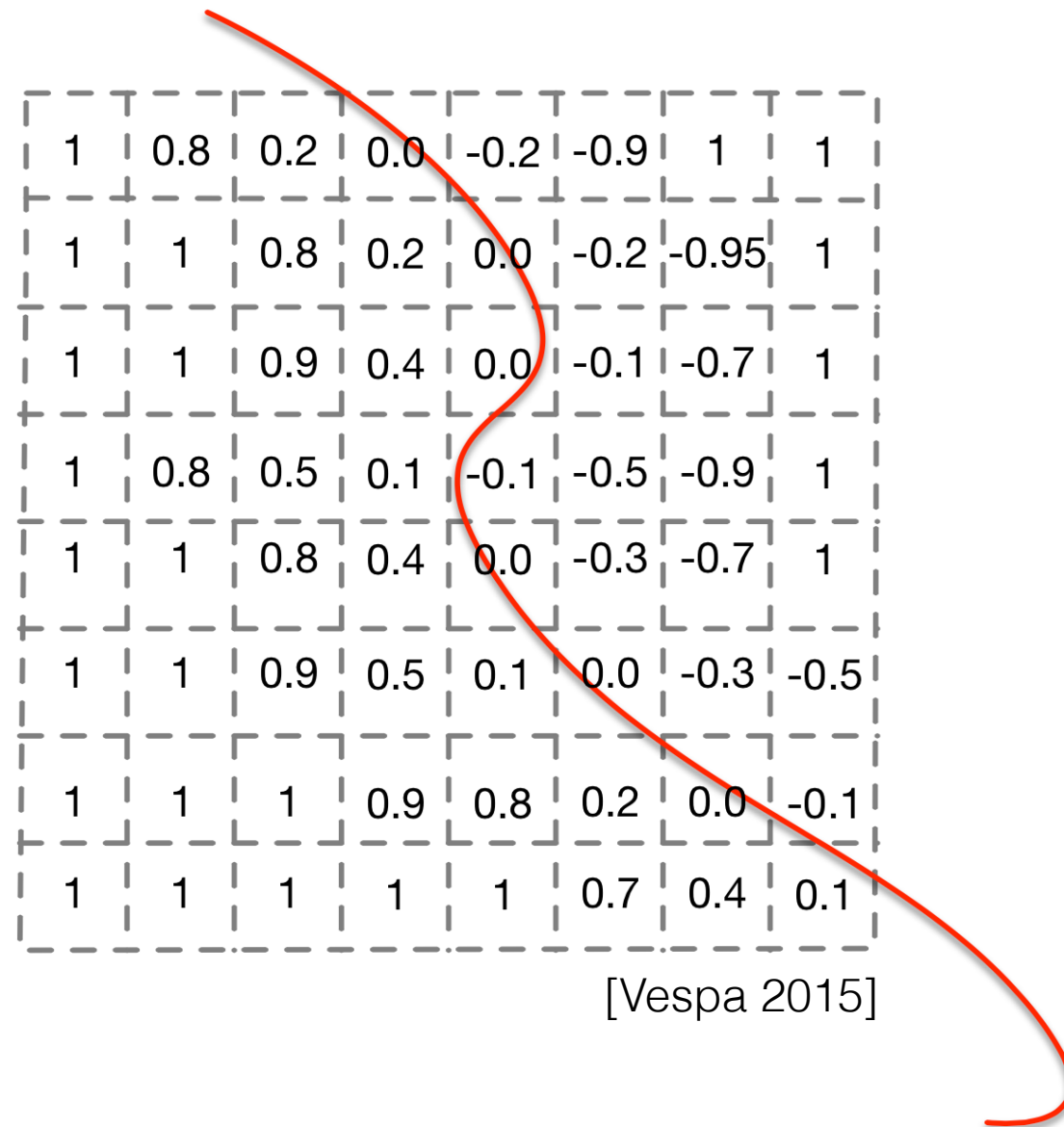


[Rusinkiewicz and Levoy 2001]

- Iterative Closest Point (ICP):
6 DoF rigid body transform from frame $k-1$ to frame k
- Iterative algorithm computing an energy function minimisation



Truncated Signed Distance Function

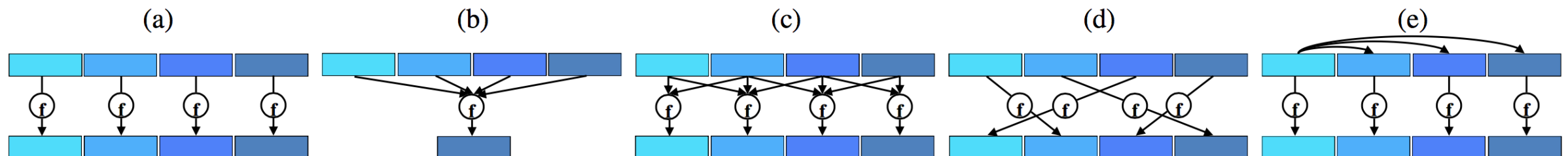


- Example of a 2D Truncated Signed Distance Function (TSDF)
- The red line shows the zero isosurface representing the best estimate of the observed surface



SLAMBench kernels

Kernels	Pipeline	Pattern	In	Out	%
acquire	Acquire	n/a	pointer	2D	0.03
mm2meters	Preprocess	Gather	2D	2D	0.06
bilateralFilter	Preprocess	Stencil	2D	2D	33.68
halfSample	Track	Stencil	2D	2D	0.05
depth2vertex	Track	Map	2D	2D	0.11
vertex2normal	Track	Stencil	2D	2D	0.27
track	Track	Map/Gather	2D	2D	4.72
reduce	Track	Reduction	2D	6x6	2.99
solve	Track	Sequential	6x6	6x1	0.02
integrate	Integrate	Map/Gather	2D/3D	3D	12.85
raycast	Raycast	Search/Stencil	2D/3D	2D	35.87
renderDepth	Rendering	Map	2D	2D	0.12
renderTrack	Rendering	Map	2D	2D	0.06
renderVolume	Rendering	Search/Stencil	3D	2D	9.18

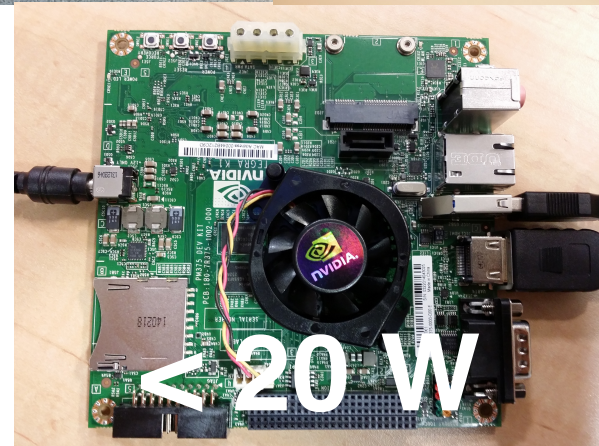
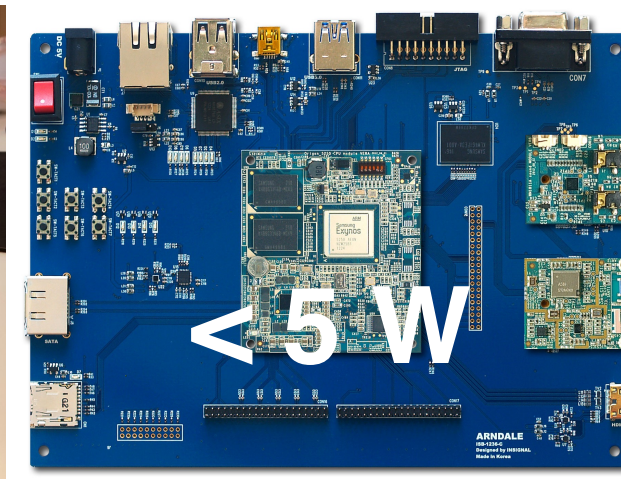


Parallel patterns: (a) Map, (b) Reduction, (c) Stencil, (d) Gather and (e) Search.



Platforms

Machine names	TITAN	GTX870M	TK1	ODROID (XU3)	Arndale
Machine type	Desktop	Laptop	Embedded	Embedded	Embedded
CPU	i7 Haswell	i7 Haswell	NVIDIA 4-Plus-1	Exynos 5422	Exynos 5250
CPU cores	4	4	4 (Cortex-A15) + 1	4 (Cortex-A15) + 4 (Cortex-A7)	2 (Cortex-A15)
CPU GHz	3.5	2.4	2.3	1.8	1.7
GPU	NVIDIA TITAN	NVIDIA GTX 870M	NVIDIA Tegra K1	ARM Mali-T628-MP6	ARM Mali-T604-MP4
GPU architecture	Kepler	Kepler	Kepler	Midgard 2nd gen.	Midgard 1st gen.
GPU FPU32s	2688	1344	192	60	40
GPU MHz	837	941	852	600	533
GPU GFLOPS (SP)	4500	2520	330	60+30 (72+36)	60 (71)
Language	CUDA/OpenCL/C++	CUDA/OpenCL/C++	CUDA/C++	OpenCL/C++	OpenCL/C++
OpenCL version	1.1	1.1	n/a	1.1	1.1
Toolkit version	CUDA 5.5	CUDA 5.5	CUDA 6.0	Mali SDK1.1.	Mali SDK1.1
Ubuntu OS (kernel)	13.04 (3.8.0)	14.04 (3.13.0)	14.04 (3.10.24)	14.04 (3.10.53)	12.04 (3.11.0)



“Performance”: accuracy

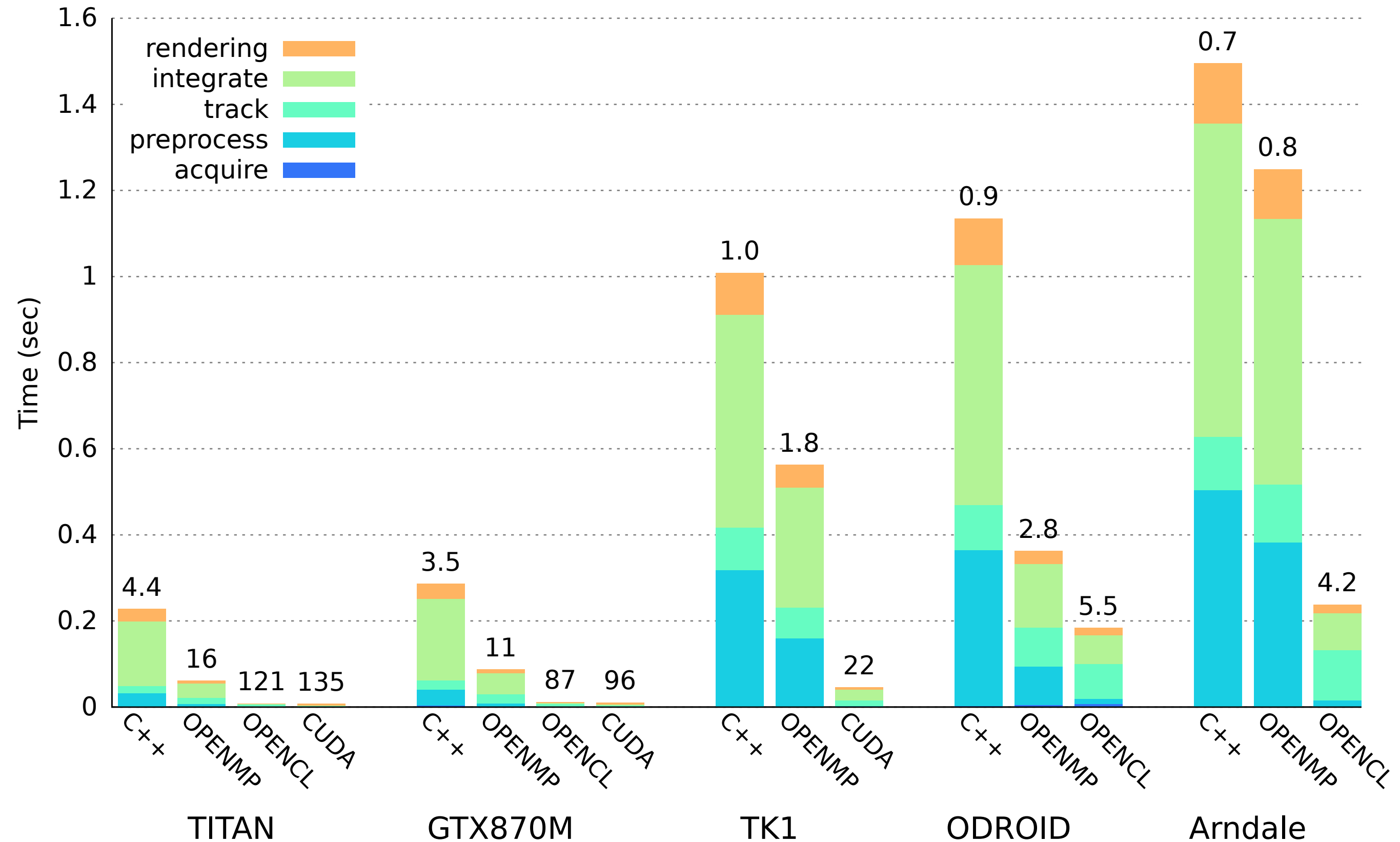
Absolute trajectory error (ATE) in cm - default algorithmic configuration

ATE in cm	TITAN	GTX870M	TK1	ODROID	Arndale
C++	2.07	2.07	2.06	2.06	2.06
OpenMP	2.07	2.07	2.06	2.06	2.06
OpenCL	2.07	2.07	n/a	2.01	2.07
CUDA	2.07	2.07	2.07	n/a	n/a

- ATE easy-to-use tool for non computer vision experts
- Semantic validation instead than bitwise accuracy

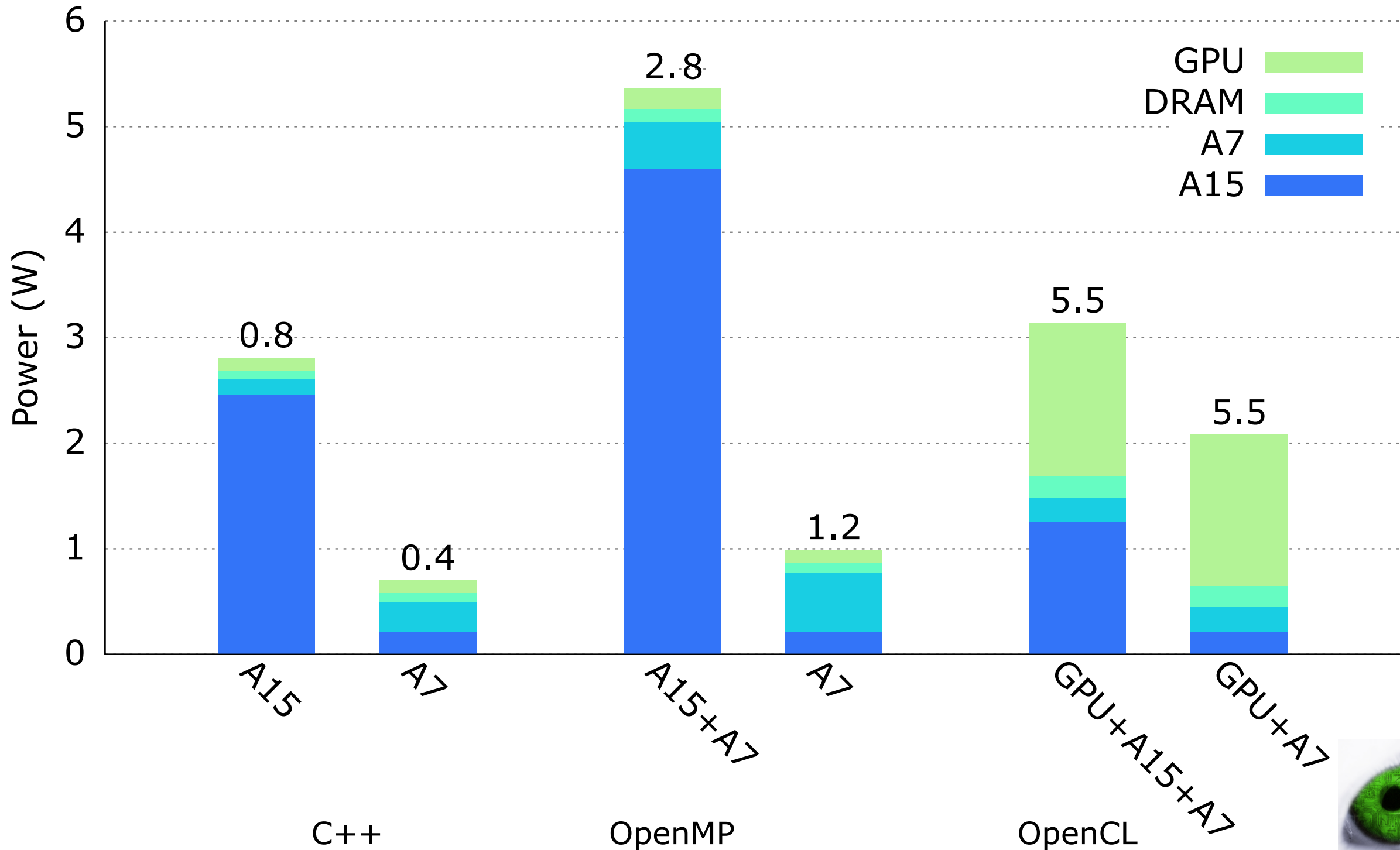
“Performance”: execution time

Mean time per frame (lower is better)



“Performance” power (ODROID-XU3)

On-board voltage/current sensors and split power rails:
power measured individually on big (A15), LITTLE (A7), GPU and DRAM



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