

Formal Verification of Data-Intensive Applications through Model Checking Modulo Theories

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Big Data is everywhere!

Software market switches to Big Data: popular technologies such as Spark, Storm, Hadoop, and NoSQL stimulates Big Data adoption.

Business issue: 65% of Big Data projects still fail (Capgemini Report 2015)

Solution:

Integrating Quality Assurance (QA) practices in application development

DICE project (<http://www.dice-h2020.eu/>) aims to define methods and tools for the data-aware *quality*-driven development of Data Intensive Applications (DIAs).

Prediction of *quality* properties of DIAs:

- ▶ Performance, reliability, *safety properties*
- ▶ Helpful early in the DIA design
- ▶ Assess the potential impact of architectural changes (iteratively)

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Apache Storm (storm.apache.org) technology – used in applications that need efficient processing of unbounded streams of data, such as event log monitoring, real-time data analytics and data normalization.

Applications that use Storm: Yahoo, Twitter, Spotify, The Weather Channel, etc.

Key **concepts**:

- ▶ **streams** – infinite sequences of **tuples** that are processed by the application
- ▶ **topologies** – directed graphs

*Streams are processed by **spouts** and **bolts**.
Topology indicates how such components are connected.*

Types of nodes:

- ▶ *input nodes* bring information into the application from the environment: **spouts**
- ▶ *computational nodes* implement the logic of the application by processing information and producing a result: **bolts**

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▶ **bolts** – receive tuples from other bolts and spouts and process them

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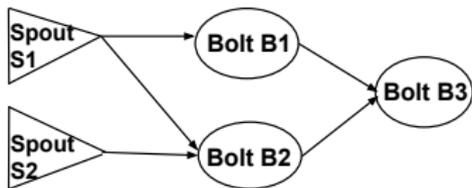
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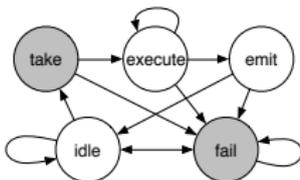
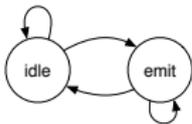
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Example of Storm topology



Finite state automata describing the states of a spout (left) and bolt (right)



Features of the topology:

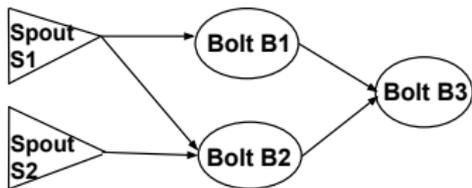
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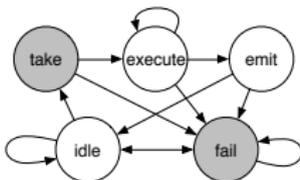
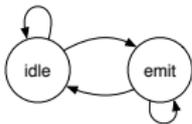
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Safety verification of Storm topologies: given queue(s) bound(s) defined by the designer, “all bolt queues have a limited occupation level”.

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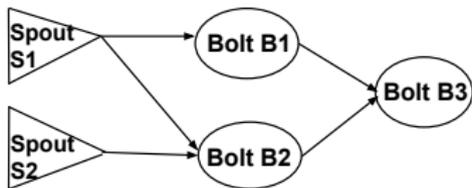
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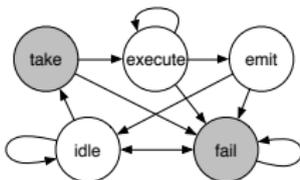
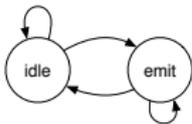
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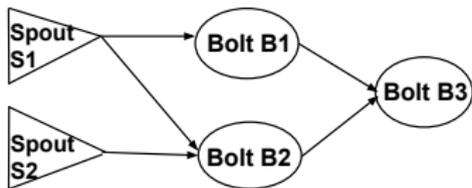
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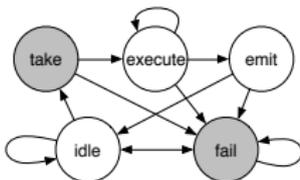
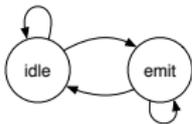
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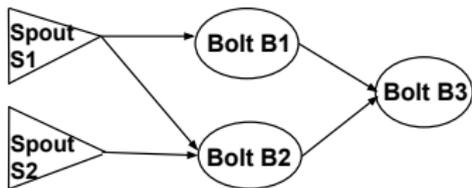
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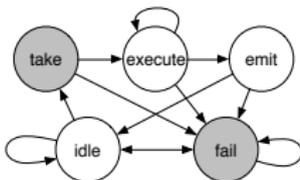
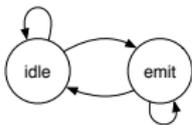
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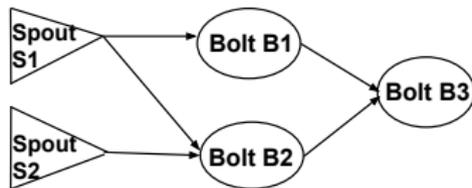
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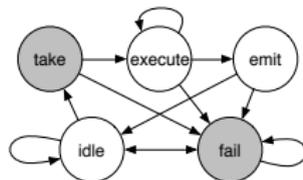
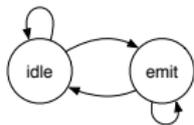
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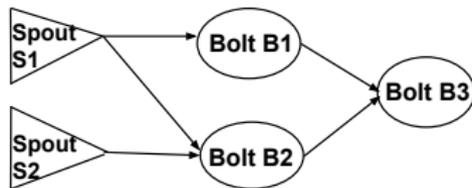
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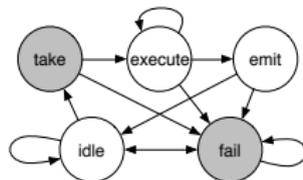
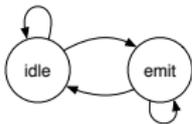
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Formalizing an array-based system means specifying:

- ▶ the set of initial states
- ▶ the ordering of the actions (by means of a transition relation)
- ▶ the set of unsafe states (the negation of the formula we want to check)

Examples (Cubicle syntax):

▶ `init (i x) { T = 0.0 && B[i,x]=I && ... }` $\rightsquigarrow \forall_{i,x} \dots$

▶

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transition spout.emit(i j)  $\rightsquigarrow \exists_{i,j} \dots$ 
requires {Tmin<Stime[i] && SubscribedBS[j,1]=True && ...}
{
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}
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▶ `unsafe(i) { L[i]>1.5 }` $\rightsquigarrow \exists_{i,j} \dots$

Symbolic representation of array-based systems: *quantified first-order logic formulae*.

Verification of array-based systems: *decision procedure* based on *backward reachability*.

Termination of the decision procedure:

- ▶ the current set of reachable states has a non-empty intersection with the set of initial states (**safety check**) \Rightarrow system is unsafe
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Formalizing an array-based system means specifying:

- ▶ the set of initial states
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- ▶ the set of unsafe states (the negation of the formula we want to check)

Examples (Cubicle syntax):

▶ `init (i x) { T = 0.0 && B[i,x]=I && ... }` $\rightsquigarrow \forall_{i,x} \dots$

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Modeling assumptions

- ▶ Focus on the behavior of the queues of the bolts
- ▶ How time parameters of the topology affect the accumulation of tuples in the queues of the bolts
 - ▶ Time frequency the spouts send information to the subscribed bolts, i.e. *minimum time between two consecutive spout emits*
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- ▶ Two approaches for abstracting the queues of the bolts:
 - ▶ each bolt has one receiving queue for each of its parallel instances (multiple queues) ($L[i, x]$)
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Formalization and Verification

The **formalization** captures the topology behavior (subscription relation, current state, modeling assumptions) through transitions:

- ▶ **discrete** transitions change the state of the topology components or updating the size of the queues of the bolts but they do not modify the value of the global time T
- ▶ **continuous** transition changes the value of the global time T and, possibly, the states of some bolts when their processing has been terminated during the last δ time units

$$\begin{array}{l} \exists_{\delta} 0 < \delta \wedge CanTimeElapse = true \wedge \\ \forall_{j,z} \left(\begin{array}{ll} T' & = T + \delta \\ P'[j, z] & = \text{if } (0 \leq P[j, z] - \delta) \text{ then } P[j, z] - \delta \text{ else } 0 \\ B'[j, z] & \dots \\ CanTimeElapse' & = false \end{array} \right) \end{array}$$

Examples of transitions and their effect:

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Formalization and verification was performed in the same framework: MCMT (<http://users.mat.unimi.it/users/ghilardi/mcmt/>), respectively Cubicle (<http://cubicle.lri.fr/>).

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Formalization and verification was performed in the same framework: MCMT (<http://users.mat.unimi.it/users/ghilardi/mcmt/>), respectively Cubicle (<http://cubicle.lri.fr/>).

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► Nondeterministic updates

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Experimental results

First attempt:

- ▶ $L[1] \geq 3$ and $T_{smin} < 1$ – **expected** result: UNSAFE

Trace: *Init* → *time_elapse* → *setDoTake_{False}* → *setDoEmit_{False}* → *spout_{emit}* →
time_elapse → *setDoTake_{True}* → *setDoEmit_{False}* →
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time_elapse → *setDoTake_{False}* → *setDoEmit_{False}* → *spout_{emit}* → $L[1] \geq 2$

- ▶ $L[1] \geq 3$ and $T_{smin} \geq 1$ – **expected** result: SAFE

Result: the verification problems lead to memory exhaustion.

Second attempt:

- ▶ $L[1] \geq 2$ and $T_{smin} < 1$ – **obtained** result: UNSAFE
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- ▶ $L[1] \geq 2$ and $T_{smin} \geq 1$ – **expected** result: SAFE

Experimental results

First attempt:

- ▶ $L[1] \geq 3$ and $T_{smin} < 1$ – **expected** result: UNSAFE

Trace: $Init \rightarrow time_elapse \rightarrow setDoTake_{False} \rightarrow setDoEmit_{False} \rightarrow \mathbf{spout}_{emit} \rightarrow time_elapse \rightarrow setDoTake_{True} \rightarrow setDoEmit_{False} \rightarrow \mathbf{bolt1}_{take} \rightarrow setDoTake_{False} \rightarrow setDoEmit_{False} \rightarrow \mathbf{spout}_{emit} \rightarrow time_elapse \rightarrow setDoTake_{False} \rightarrow setDoEmit_{False} \rightarrow \mathbf{spout}_{emit} \rightarrow L[1] \geq 2$

- ▶ $L[1] \geq 3$ and $T_{smin} \geq 1$ – **expected** result: SAFE

Result: the verification problems lead to memory exhaustion.

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Current and Future Work

- ▶ Refinements of the presented model, linear topologies \rightsquigarrow limiting the analysis to well-founded transition systems
- ▶ New model to capturing relevant properties of distributed systems, e.g. tuple order is compatible with tuple time

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